



Rajeev Kumar

Professor, School of Computer & Systems Sciences (SC&SS)

Jawaharlal Nehru University (JNU), New Delhi 110 067

Voice : (91)-11-2673-8789 (Off.); 95-9905-3655 (Mob.)

Email : [rajeevkumar.cse @ Gmail.com](mailto:rajeevkumar.cse@gmail.com), rkuarcs@jnu.ac.in

[GitHub](#) | [LinkedIn](#) | [Google Scholar](#) | [JNU Web](#) | [Research Gate](#) | [ORCID](#)

Rajeev Kumar is a Professor of Computer Science at JNU New Delhi. He holds a PhD degree in Computer Engineering from **Univ. Sheffield**, an M.Tech. degree in Computer Sc. & Tech. from **Univ. Roorkee** (now, IIT Roorkee), and an M.Sc. degree in Physics (Electronics) from **Univ. Allahabad**. His primary education was in a river-side *Gurukul*, Shri Dugdeshwar Sanskrit Mahavidyalaya at Pilibhit.

Earlier, he served the Indian Institutes of Technology (**IITs**) at **Kharagpur & Kanpur** and Birla Institute of Technology & Science (**BITS**) **Pilani**. Prior to his academic tenure, he worked as a Scientist in the Department of Science and Technology (**DST**) Hyderabad and New Delhi and Defence R & D Organization (**DRDO**) Dehradun. He has done a visiting assignment at National Semiconductors (**NSC**) Germany and has visited several universities in UK, USA, Germany, Switzerland, Japan, S. Korea, and Singapore.

Research: He has four decades of experience in research and teaching. His research interests include machine learning, scientometrics, programming language, software system, multimedia & evolutionary optimization. He has published over 200 research articles in international journals and conferences. He has supervised fourteen doctoral theses, over eighty master theses, and hundred undergraduate projects. He is currently supervising eight doctoral and three master students. He has worked on industrial-funded projects from Microsoft and NSC, the NSC project was in close collaboration with their units in USA, Germany, and Israel. He founded *Data to Knowledge* (D2K) Lab in JNU. His h-index, on Google Scholar, is 25.

Teaching: Rajeev has designed and taught several Computer Science & Engineering courses ranging from freshman to senior graduate level. He has taught programming (C/C++/C#/Java/R/Python), core and advanced (Data Struct., Algorithms, Compiler, Software Engg, Pattern Classification, Machine Learning), application (Image/Video Processing, Computer Vision, Multimedia Sys.), and specialized courses (Statistics, Evolutionary Comp, Research Method, etc.). In JNU, he introduced a course on *Academic Ethics*. His teaching focuses on enhanced student engagement for active learning, creativity, and innovation.

Academic Leadership: He served as a Senator in IITs Kharagpur/Kanpur and BITS, member of the Court and Academic Council in JNU, and several other academic bodies, e.g., Board of Studies, Statutory Comm. of Institutes/Universities. He worked for reforms/revisions in curriculum, academic structure, regulations, and ordinances. He worked as an active member, a crisis handler in most committees. He is known for out-of-box solutions for complex problems in simpler manner.

Public Policy: Rajeev has contributed significantly towards framing public policies for higher education. He reformed policies for IITs admission examinations bringing transparency and accuracy through well-defined processes. He was instrumental in introducing a **common examination** and **common counseling** (JoSAA) for admissions in all IITs/NITs/IIITs/CFTIs in India. Almost all admission & competitive examinations, in most disciplines, have been reformed following this model. His proposal for an independent body led to the setting of the National Testing Agency (**NTA**). The inclusion of common examinations in NEP 2020 is a testimony of his work, leading to **CUETs** in Central Univ. His proposal for **common counselling in CUs** is being worked-out. India's Supreme Court hailed him as *one of the many unsung heroes who helped in improving the system*.

He defined quantitative Academic Performance Indices (**APIs**) in 2009 for engineering faculty. He co-authored/authored *output based quantifiable* accreditation guidelines, from 2009 to 2011, for UG engineering programs. These were in public use since 2011; this also led to setting up National Board of Accreditation (**NBA**) as an autonomous body. This work led India acquire permanent membership of **Washington Accord** in June 2014. Revised guidelines are in public use now.

Services: He has been a regular reviewer in several journals/conferences and served in Technical Program Committees. He delivered several keynote addresses, invited lectures, and tutorials in conferences.

His academic and administrative experience of four decades spanning Univ./Insts. across India and abroad and combined with his 24/7 work-ethics as a team player with difference, *reform-centric*, and *student-oriented* leadership, *legal acumen*, and having *wider* perception, is an asset to the Institute he serves.

About Me:

- **Education:** I obtained Ph.D. in Computer Engineering from Univ. Sheffield, England, M.Tech. in Computer Sc. & Tech. from Univ. Roorkee (now, IIT Roorkee), and MSc in Physics (Electronics) from Univ. Allahabad.
 - My PhD thesis was nominated for the distinguished CS thesis award in UK.
 - I was awarded Univ. Medal for being the first rank holder in MTech.
- **Experience:** I have over four decades of research and teaching experience working in:
 - Academic Institutions: BITS Pilani, IIT Kharagpur, IIT Kanpur, and JNU New Delhi.
 - Research Organization: DRDO (DEAL), Dehradun.
 - Industry: National Semiconductors, Germany (Visiting).
- **In Academic Bodies:** I have been an active and contributory member in most types of academic bodies:
 - National:
 - Chief Examiner, GATE Computer Science (IITs)
 - Chair/Member, NBA Accreditation Teams: Assorted
 - Institute:
 - Univ. Court, Academic Council (JNU): 1 years
 - Senate (IIT Kharagpur, BITS): 11 years
 - Senate Sub-Committee: Undergraduate Program (IIT Kharagpur): 8 years
 - Research Board (BITS): 2 years
 - Lib. Comm. (BITS): 2 years
 - Department:
 - CSE Academic Comm. (IIT Kharagpur, BITS, JNU): 16 years
 - Doctoral Scrutiny Committee (IIT Kharagpur): Assorted
 - Other Institutes (selected recent ones): Banasthali Univ., Poorvanchal Univ., etc.: 2 years, IIT Patna
- **In Academic Admin.:** I have been actively involved in a variety of the following academic administration; I was also a crisis handler:
 - National:
 - Chair, NBA UG Engineering Accreditation Guidelines Draft Comm.
 - Reforms in JEE and JEE (Adv.) resulting in a common examination for CFTIs.
 - Formulation of Common Counselling for all UG engineering admissions, JoSSA.
 - Conceptualization of National Testing Agency (NTA).
 - Institute:
 - Anti-Plagiarism Policy Draft Comm., (JNU): 1 year
 - Intellectual Property Management Comm. (JNU): 1 year
 - Department/School:
 - Prof.-in-Charge, Data-to-Knowledge (D2K) Lab, SC&SS, JNU: 6 years
 - CSE Admin. Committee (IIT Kharagpur): 5 years
 - Professor-in-Charge, Dept. CSE Lib. (IIT Kharagpur): 2 years
 - Faculty – Student Comm. (JNU): 4 years
 - International Level
 - Program Chair, Publication Chair, Publicity Chair, etc. for international conferences.
- **Research Leadership:** Directed large teams of Research Associates and Doctoral/Master's thesis students in IIT Kharagpur and JNU. I founded Data to Knowledge (D2K) Lab in JNU.
 - In JNU, I solely supervised more than twenty thesis students at a time.
 - There were few tens of Machine Learning Course students worked for their projects.
 - The D2K Lab produced significant number of quality research papers, published in decent Journals and presented in conferences.
 - There were seminars by students and external experts.

- **Public Policies:** I have maintained a delicate balance between stakeholders, namely Government and academicians while developing impact-driven public policies:

- As Chair of an NBA (National Board of Accreditation) team, I drafted outcome-based UG-Engineering accreditation guidelines. This led to India's entry into the Washington Accord and worldwide recognition of Indian UG-Engineering degrees.
- Pioneered transparency, common examination, and common counselling in Engineering admissions. This has been adopted by almost all other admissions in the country. The inclusion of common examinations in NEP 2020 is a testimony of my work. My proposal for Common Examinations for Central Universities introduced CUETs in CUs.
- My proposal for an independent body led to the founding of the National Testing Agency (NTA).
- Proposed Common Counselling for all admissions in Central Universities. (Under consideration)
- Proposed measures and processes for mental well-beings, early alert generations, and grievance redressals for preventing unfortunate incidences in IITs. (Under consideration)

- **Teaching:** I subscribe to an *active* learning style of teaching. I draw upon fundamental concepts as well as emerging technologies to keep the subject material relevant and interesting.

- Taught, developed, and restructured core and advanced courses in Computer Sc. & Engg. for UG and PG students in IIT Kharagpur, BITS Pilani, JNU.
- Introduced university-wide course on Academic Ethics at JNU.
- Delivered tutorials for international conference ACM GECCO: USA, UK (funding: Google Inc.)
- Keynote/Invited addresses in Faculty Development Programs/Short Term Courses.
- Conceptualized Dual-degree (BTech + MTech) program combining Comp Sc with Social Sc.

- **Research:** My research interests include machine learning, scientometrics, programming language, software systems, multimedia, evolutionary optimization, and academic governance & ethics.

- Published over 200 research articles in international journals and conferences.
- Research papers in leading international conf. in USA, UK, Germany, Canada, Japan, S. Korea, etc.
- Invited/research talks in several universities in USA, Germany, Switzerland, Japan, S. Korea, etc.
- Key-note & tutorial speakers in several int. conferences. Reviewer in several journals, conferences.
- Supervised 14 doctoral, 80 master theses. Currently supervising 8 doctoral and 3 master students.
- Worked on industrial-funded projects from Microsoft and National Semiconductors (NSC).
- Awarded *Test of Time* award in ISCE 2022 for the paper presented in ISEC 2011.
- Founded *Data to Knowledge* (D2K) Lab in JNU's School of Computer & Systems Sciences.
- *h-index*, on Google Scholar from incomplete data, is 25, which is good in sub-fields of CS.

- **Industry Collaboration: Research**

- National Semiconductors (Germany, Israel) for developing System SW tools, simulators, compilers, etc. for their CR16X series of processors. In this work, Prof. Richard Brown, Univ. Michigan USA was another collaborator. We made per week deliveries for their design engineers, which were regularly used by many categories of stakeholders, from system designers to application users. (2001 – 2006)
- Member, IT Advisory Comm., Canara Bank (2022 onwards)

- **Industry Collaboration: Teaching**

- Developed courseware for .NET/C# as emerging technology (funding: Microsoft Inc. USA). (2004-07)
- Taught Multimedia & OOS Courses to employees of Wipro & SW industries at Bangalore/Hyderabad under BITS Industry Collaboration (Off-Campus) program. (1997- 2000)

I have undertaken a wide spectrum of academic and administrative responsibilities over the past 37 years in premier academic and research institutions in India and abroad. My initial learning at DRDO: *technology transformation for end-users*, guided me towards *in-depth knowledge* and *simplified governance*. These strengths combined with my *24/7* work-ethics as a team player, inside-outside perception, and legal acumen-ship will be an asset for academic leadership.

Personal Information

- Nationality : Indian
- Date of birth : March 12, 1959
- Marital Status : Widower with two children

Education

- Ph.D. in Computer Engineering (1997)
University of Sheffield, UK
Thesis "Feature Selection, Representation & Classification" nominated for best Thesis award in UK
- M.Tech. in Computer Science & Technology
University of Roorkee (now, IIT Roorkee), India
University Medalist being the first ranker
- M.Sc. in Physics (Electronics specialization)
University of Allahabad, India
Third Rank in the University
- B.Sc. in Maths, Physics & Chemistry
Rohilkhand University, India

Research Scholar

- Commonwealth Research Scholar (March 1994 – Feb 1997), University of Sheffield, UK
- UGC Research Scholar (July 1980 – Nov 1983), University of Allahabad, India

Employment

- June 2015 onwards: Professor, School of Computer & Systems Sciences, Jawaharlal Nehru University (JNU), New Delhi, India
- April 2007 – June 2015: Professor, Department of Computer Science & Engineering, Indian Institute of Technology (IIT), Kharagpur, India
- Dec 2000 – April 2007: Assoc. Professor, Department of Computer Science & Engineering, Indian Institute of Technology (IIT), Kharagpur, India
- Feb 2000 – Dec 2000: Professor, Department of Computer Science & Information Systems, Birla Institute of Technology & Science (BITS), Pilani, India
- April 1997 – Jan 2000: Assoc. Professor, Department of Computer Science & Information Systems, Birla Institute of Technology & Science (BITS), Pilani, India
- June 1986 – March 1995: Scientist, Defense R & D Organization (DRDO), Defense Electronics Applications Lab (DEAL), Dehradun, India
- Dec 1983 – June 1986: Scientist, Department of Science & Technology (DST), Survey of India, Hyderabad / New Delhi, India

Visiting Positions

- July 2013 – May 2014: Visiting Professor, Department of Computer Science & Engineering, Indian Institute of Technology (IIT), Kanpur, India
- June 2005 – May 2006: Visiting Faculty, Department of Computer Science & Engineering, Indian Institute of Technology (IIT), Kanpur, India
- May 2002 – July 2002: Visiting Design Engineer, National Semiconductors GmbH, Germany

Teaching Courses (Graduate and undergraduate)

- Foundations of Computing (C, C++, Java, Python)
- Data Structures & Algorithms (Core & Adv.)
- Compiler Construction (Core & Adv.)
- Object-Oriented Programming (C++, Java, C#)
- Object-Oriented Analysis & Design (UML)
- Programming Methodology
- Multimedia Systems
- Machine Learning
- Research Methodology
- Academic Ethics
- Programming & Data Structures (C, C++, Java)
- Design & Analysis of Algorithms
- Software Engineering (Core & Adv.)
- Object-Oriented Language Implementation
- Object-Oriented Software Engineering
- Software Architecture
- Image & Video Processing
- Evolutionary Computing
- (Bio-)Statistics (with R) @ Life Sciences
- Technical Writing

Research Interests

- Machine Learning: Outlier Detection, Medical Image Analysis, Edu. Data Mining, & Scientometrics.
- Programming Languages & Software Engineering.
- Multimedia & Social Networks.
- Evolutionary Multiobjective Combinatorial Optimization & Nature Inspired Algorithms.

Selected Sponsored Research & Industrial Projects

Educational Data Mining	2018 - 19	INR 500K	University Grants Commission
Virtual Lab – Programming & Data Structure Lab	2010 – 12	INR 1500 K	Min. Human Resource Dev., Government of India
NPTEL Video Course Development – Compiler, Multimedia	2010 – 13	---	Min. Human Resource Dev., Government of India
Object Oriented (C#/.NET centric) Courseware Development	2004 – 07	USD 22 K	Microsoft Corp., USA
Multiobjective Evolutionary Algorithms for Combinatorial Optim.	2002 – 07	INR 1000 K	Min. Human Resource Dev., Government of India
Software Tools for Embedded Systems (co-PI)	2003 – 06	USD 100 K	National Semiconductors Corp., Germany/USA
Software Tools for CR Processors (co-PI)	2001 – 03	USD 225 K	National Semiconductors Corp., Germany/USA
Convergence of Multiobjective Optim.	2001 – 03	INR 100 K	IIT Kharagpur

Theses/Projects Supervision

	Completed	Ongoing
Doctoral	8+6*	8
Graduate (by research)	4	--
Graduate (M.Tech., MPhil, MSc)	~ 70 + 10*	3
Undergraduate projects	~ 100 + 20*	--

*: In joint supervision.

Publications

Book	1
Edited Books (Conf. Proceedings)	3
Chapters in Edited Books	4
Published Tutorials	3
Journal Research Articles	80
International Conference/Workshop Research Articles	~110
National Conference/Workshop Research Articles	~20

International Work / Collaboration: Research

Visitor	GaTech Atlanta, US	ML, PL	Feb-Mar 2023
Visitor	NUS, Singapore	ML, PL	June, Dec. 2022
Visitor	UPenn, Rice Univ, USA	Prog. Lang., ML	May 2022
Visitor	NUS, Singapore	Multimedia, ML	July 2019
Visitor	Rice Univ., USA	Machine Learning	Mar. 2018
Visitor	Univ. Sheffield, UK	Evo. Algo. Comb. Optim.	July 2007
Visitor	GIST, S. Korea	Evo. Algo. Comb. Optim.	Mar. 2007
Visitor	TU Darmstadt, Germany	Prog. Lang., Multimedia	Sep. 2006
Visitor	Aizu Univ., Japan	Evo. Algo. Comb. Optim.	Jan. 2005
Visitor	UIUC, USA	Multimedia, Evo. Algo	July 2003
Visitor	ETHZ Switzerland	Multi-obj. Evo. Algo.	June 2002
Visitor	MIT, CMU: USA	Machine Vision	July 1996
PhD, TA	Sheffield Univ., UK	Comp. Engg.	Mar 1994 to Apr 1997

International Industry Collaboration: Research

Design Engineer	NSC Germany	SW Sys. Tools: Compiler, Simulator etc.	May – July 2002 Oct. 2001
-----------------	-------------	--	------------------------------

Industry Collaboration: Teaching

Microsoft Research USA	Object Oriented (C#/.NET centric) Courseware	2004 – 07
Wipro & SW Industries	Object Oriented Sys Development, Multimedia Systems	1998 – 2000

Public Policy: In the Service of the Nation

- Worked for Common Examination and Common Counselling in Central Universities (2018 onward)
- Pioneered Common Examinations and Common Counselling for UG Admissions in IITs, NITs, IIITs, CFTIs, etc. with wide-spread reforms in IIT-JEE (2006 - 15) (Based on IIT-JEE Reform Proposals, Sep. 06, Oct. 08, Feb. 10, Sep. 10, July 11, Aug. 14, Nov. 15)
- Authored: NBA Accreditation Guidelines for UG Engg. Programs, May 11. In Public use: July 2011 to Jan. 2013. This led India acquiring permanent membership of Washington Accord in June 2014. <http://www.nbaind.org/Files/engineering-programs.pdf>
- Co-Authored: NBA Revised guidelines for Accreditation UG Engg. Programs, July 09. In Public Use: July 2009 to June 2011. <http://www.nbaind.org/Files/Report%202009.pdf>

External Services (a few recent ones)

- Expert, NTA Tech. Comm. (2023)
- Expert, IT Advisory Comm., Canara Bank (2022 Onward)
- Expert, Tech. Comm., Agriculture Scientists Recruitment Board (ASRB), Dept. Agri. Res. Edu. (2022)
- Ext. Member, Board of Studies, Banasthali Univ. (2019 - 22), Poorvanchal Univ. (2018 - 20)
- Member, Apex Committee for DRDO Awards 2016, Dept. Def. R & D, Govt. of India
- Chairman/Expert Member, National Accreditation Board (NBA) Visiting Committees to Engineering Institutions (2007 onwards)
- CSE Expert, UPSC, New Delhi

Academic Administration @ JNU New Delhi (2015 onwards)

- Founder Director, *Data to Knowledge* (D2K) Lab, SC&SS, JNU (2016 onwards)
- Member, Faculty – Student Comm., School Comp. & Sys. Sci. (2015 – 2019)
- Member, Univ. Anti-Plagiarism Policy Draft Comm. (2016 - 17)
- Member, Univ. IPR Policy Draft Comm. (2017)
- Chairman, AICTE Programme Approval Comm., School Comp. & Sys. Sci. (2015-16)
- Member, MCA Re-structuring Comm., School Comp. & Sys. Sci. (2016).

Academic Administration @ IIT Kharagpur (2001- 2015)

- Member, Senate (2007 – 15),
- Undergraduate Program Evaluation Committee (UGPEC): CSE coordinator (2001-05); (2007 – 11)
- Member, CSE Academic Committee, IIT Kgp (2001 – 05; 2007 – 11)
- Member, CSE Dept. Administrative Committee, IIT Kgp (2004 - 05, 2006 - 10)
- Professor-in-Charge Library, CSE and Liaison with Central Library, IIT Kgp (2006 – 08)
- Member, Doctoral Scrutiny Committee(s);
- Chief Examiner, GATE Computer Sc (2005)
- Professor-In-Charge, JEE Examination Centre(s),
- Faculty Advisor/Counselor/Mentor, Faculty, PGDIT and PGDST Programs

Academic Administration/Work @ BITS Pilani (1997 – 2000)

- Updated CS and IS Curriculum and Introduced/Restructured Courses
- Introduced Lab Components in CS Courses
- Faculty, Distance Education @ Wipro and others
- Resource Faculty, Teaching Workshops
- Member – Academics, Doctoral, Research & Consultancy, Recruitment, Library, Senate.

New Courses Developed / Restructured

- Bio-Statistics [2018] @ JNU – *bringing computation with R*
- Academic Ethics [2016] @ JNU
- Software Engineering [2006] – *bringing synergy with Trusted Computing* @ IIT Kharagpur
- OO Sys. Implementation [2005] – *synergy in PLI, SE, VEE for trusted computing* @ IIT Kanpur
- Foundations of Computing [2005] – *with Java* @ IIT Kanpur
- Multimedia Systems [1998, 2001] @ BITS Pilani, IIT Kharagpur
- Evolutionary Algorithm [1999] @ BITS Pilani
- Programming Language & Compiler Construction [1998] @ BITS Pilani
- Data Structures & Algorithms [1998] @ BITS Pilani
- Computational Intelligence [1998] @ BITS Pilani

Awards & Professional Recognition

- Affiliation with Professional Bodies
 - Fellow, IETE, India

- Fellow, ISSA, India
- Senior Member, ACM, USA
- Senior Member, IEEE, USA
- Entrusted with Drafting Washington Accord based UG Engg. Accreditation Guidelines
 - Drafted the 3rd fully Revised Edition (May 11); In National Use since May 2011.
 - India acquired permanent membership of Washington Accord in June 2014.
 - Conducted National Awareness Workshops for Institutes and Assessors (2009)
- Tech. Paper Reviewer, Conference Organization, Conf. Program Committee Member : In many
- RTI Citizen's Runner-up Award for Public Services (2009)
- Karmaveer Chakra (2013)
- ISEC's *Test of Time* Award for the ISEC 2011 Paper (2022)

Tutorials

- Evo. Multi-criteria Optim. @ GECCO-07, London; GECCO-08, Atlanta; GECCO-09, Montréal.
- Architecture exploration for embedded system design @ HiPC-04, Bangalore.
- Multimedia system design for QoS @ HiPC-03, Hyderabad.
- Multimedia system @ ITPC-03, Kathmandu.

Invited Talks / Seminars

Machine Learning & Data Analytics

- D2K: Machine Learning & Data Intelligence @ FDP, JIIT Noida, Jan. 2023.
- Evolution of a perceptron to massively connectionist deep learning architecture with applications @ UPES Dehradun, Dec. 2021.
- Evolving a perceptron to massively connectionist deep learning architecture with applications @ Rajiv Gandhi University, Itanagar, Sep. 2021
- Intro. to Prob. Theory in ML @ NIT Jalandhar, Sep. 2020
- The Pedagogy of AI for Multidisciplinary Students @ Huawei AI Educator Symp, Mumbai, Dec. 2019
- ML: Research Directions and Applications @ FDP, KEIT Ghaziabad, June 2019.
- AI: Issues and Current Trends @ World TelComm. & Info. Society Day, C-DoT Delhi, June 2018.
- ML: Issues & Research Directions @ ML Workshop, IMS Ghaziabad, Jan. 2018.
- Trends in Outlier Detection: Issues & Challenges @ Data Mining STC, NSIT Delhi, Nov. 2017.
- Data-mining by meta-learning @ IARCS Course, 2004.
- Meta-learning of high-dim. spaces for scaling and generalization in data mining @ IIIT-H, 2000.
- Scaling and generalization in data mining by meta learning of data patterns @ IETE Pilani, 1999.
- Hybrid computational intelligent systems @ KanGAL, IIT Kanpur, 1997.
- Hierarchical organization of intelligent models @ TIET Patiala, 1997.
- Hybrid Intelligent models @ BITS Pilani, 1997.

Education, Sc. & Tech., E-Office & Governance

- Ethical Writing: Learning from experiences of day-to-day live examples @ ICARS Delhi, June 2020.
- Scientometrics and Publication Ethics @ VJTI Mumbai, Dec. 2019.
- E-Governance with AI / ML @ Refresher Course, UGC-HRDC, JNU, July 2018, Aug. 2019.
- Crisis in HEIs in India: A Technological Perspective @ Press Club of India, JNUTA, Nov. 2018.
- Evolution of Paperless PhD: A journey from stone-age to paperless age, in Seminar series on Decoding Sc. & Tech. for everyone @ JNU, Oct. 2018.
- Academy Autonomy: Gurukuls and Today's HEI @ ISSCA Workshop, Delhi Univ., Feb. 2018.
- Academic Autonomy vs. Quality in Education: from Gurukul to Today's Institutions. A plenary talk in Workshop for Growth of Science & Technology, VBS Purvanchal Univ., Sep. 2017.

- Personal Computers to Personalized Computing for Socializing and Banking: A Paradigm Shift in Computing & Comm. Research, Ind. Social Science Congress (ISSC), Andhra Univ., Mar. 2015.

Programming Languages & Software Systems

- Software Engineering and Runtime Systems @ NIT Rourkela, May 2009.
- Object oriented software engg. :: concepts and practices @ NIT Durgapur, Thapar Univ., 2008.
- Virtual execution environment for trusted computing @ NIT Rourkela, October 2008.
- Object oriented software engineering:: A Lecture series @ JIITU Noida, September 2008.
- Programming pearls and pitfalls @ JIITU Noida, January 2008.
- Multiple polymorphic arguments in object-oriented lang. @ IIT Delhi, TU-Darmstadt, 2006.
- Object oriented language implementation course – a working proposal @ Microsoft, 2006.
- Reusable plug-in software components for dependable systems @ EuroIndia, 2004.
- Software tools for extensible CompactRisc processors @ National, Munich, June 2002.
- Message dispatch in object-oriented systems @ IIIT Hyderabad, 2000.

Evolutionary Multiobjective Combinatorial Optimization (EMCO)

- EMCO: Issues and Research Directions @ UGC HRDC, JNU, Sep. 2015.
- EMCO: A keynote talk @ ICCC, Noida, Sep. 2008.
- EMCO - solving hard problems @ Gwangju IST, S. Korea, Mar. 2007.
- Solving hard problems in EMO - a practitioner's approach @ KanGAL, IIT Kanpur, 2006.
- Convergence in multiobjective genetic optim. & combinatorial prob. @ IlliGAL UIUC, 2003.
- Convergence in multiobjective genetic optim. using rank-histograms @ ETH Zurich, June 2002.
- Population driven computational paradigm for search and optimization @ BITS Pilani, 2000.
- A practical approach to EMO @ IIT Roorkee, 1997.

Multimedia & Embedded Systems

- Video coding – history & practices: Keynote talk @ Int. Conf. SP, SATI Vidisha (2016).
- Video transcoding: algorithms and architectures @ TU-Darmstadt, 2006.
- Networked multimedia @ DPN Conf., Kharagpur, 2004.
- Globalization through miniaturized multimedia devices @ Allahabad Univ. (2004).
- Embedded System Design @ Galgotias Noida, 2004.
- Design space exploration tools for embedded systems @ CEERI (2004), EuroIndia (2004).
- Transcoding and QoS for multimedia traffic @ MONET, UIUC (2003).
- Teaching with tech.: multimedia in computer aided learning @ Kendriya Vidyalaya (2003).
- On QoS: reality check @ HiPC Trusted Internet Workshop, 2002.
- Triplet geometric representation: novel local invariants for robust recognition @ CMU, 1996.

Research Publications (in reverse chronology)

Book

- [1] Soham S. Chakraborty, Rajeev Kumar, and PP Chakrabarti. Static Analysis and Optimization of Object-Oriented Systems – Concepts and Approaches. 2012. Lap Lambert Academic Publishing GmbH, Germany. ISBN 978-3-8484-1353-9.

Edited Volumes (Conf. Proceedings)

- [2] Sanjay Kumar Jena, Rajeev Kumar, Ashok Kumar Turuk, and Manoranjan Dash (2011) Proc. Int. Conf. Communication, Computing, and Security (ICCCS), Rourkela, India, Feb. 12-14, 2011. ACM.
- [3] Sanjay Ranka, Srinivas Aluru, Rajkumar Buyya, Yeh-Ching Chung, Sumeet Dua, Ananth Grama, Sandeep K. S. Gupta, Rajeev Kumar, Vir V. Phoha (2009) Proc. Contemporary Computing – 2nd Int. Conf., IC3 2009, Noida, India, August 17-19, 2009. Springer.
- [4] Ajit Pal, Ajay Kshemkalyani, Rajeev Kumar, and Arobinda Gupta (2005) Distributed Computing - Proc. Int. Workshop Distributed Computing (IWDC). LNCS vol. 3741, Dec. 2005. ISBN 3-540-309-59-4. Springer.

Edited Book Articles

- [5] Sonal Tuteja and Rajeev Kumar (2020) An Architecture for Data Unification in E-commerce using Graph. In: Kapur P. et al. (eds) *Strategic System Assurance and Business Analytics. Asset Analytics (Performance and Safety Management)*, chapter 30, pages 407-417. Springer, Singapore. doi: 10.1007/978-981-15-3647-2_30
- [6] Akanksha Mukhriya and Rajeev Kumar (2018) Exploring Ensembles for Unsupervised Outlier Detection: An Empirical Analysis. In: Chakraverty S., Goel A., Misra S. (eds) *Towards Extensible and Adaptable Methods in Computing*, pages 225 – 237. Springer. doi: 10.1007/978-981-13-2348-5_17
- [7] Rajeev Kumar and PK Singh (2007) Pareto Evolutionary Algorithm Hybridized with Local Search for Biobjective TSP. In Hybrid Evolutionary Systems: Chap 6, 2007. Studies in Computational Intelligence Series, Springer.
- [8] Rajeev Kumar (2004) On Machine Learning with Multiobjective Genetic Optimization. In Carlos A. Coello Coello, and Gary B. Lamont (Eds.), Applications of Multiobjective Evolutionary Algorithms, Chap. 17: 393 - 425, December 2004. ISBN 981-256-106-4. World Scientific.

Published Tutorials

- [9] Rajeev Kumar. A Tutorial on "Evolutionary Multiobjective Combinatorial Optimization (EMCO)" -- A Specialized Tutorial. In Proc. Genetic and Evolutionary Computation Conference (GECCO-2009), Montréal, pp. 3413-3436, 09 July 2009. ACM.
- [10] Rajeev Kumar. A Tutorial on "Evolutionary Multiobjective Combinatorial Optimization." A Specialized Tutorial in Genetic and Evolutionary Computing Conference (GECCO), Atlanta, pp. 2805 - 2828, 13 July 2008. ACM.
- [11] Rajeev Kumar. A Tutorial on "Evolutionary Multiobjective Combinatorial Optimization." A Specialized Tutorial in Genetic and Evolutionary Computing Conference (GECCO), London, pp. 3366 - 3390, 08 July 2007. ACM.

Research Articles in Refereed Journals, Conf Proceedings, and Newsletters

- [12] Neeraj Pathak and Rajeev Kumar (2023) Entropy guided evolutionary search for solving Sudoku. *Progress in Artificial Intelligence*. Springer. <https://doi.org/10.1007/s13748-023-00297-7>
- [13] Roopam Sadh and Rajeev Kumar (2023) Transformation and classification of ordinal survey data. *Computer Science Journal*. AGH Univ Sc Tech, Poland.
- [14] Sonam Chhikara and Rajeev Kumar (2023) Information theoretic steganalysis of processed image LSB Steganography. *Multimedia Tools & Applications*. Springer. <https://doi.org/10.1007/s11042-022-13931-8>

[15] Roopam Sadh and Rajeev Kumar (2022) Dimensional inadequacy of rankings: Exploring substantial and meta-quality dimensions for HEIs. *Academia* 26: 25-48. Higher Edu. Policy (HEP) Net, Europe. <https://doi.org/10.26220/aca.3948>

[16] Sonal and Rajeev Kumar (2022) Query driven graph models in E-commerce. *Innovations in Systems & Software Engineering*. Springer. <https://doi.org/10.1007/s11334-021-00421-7>

[17] Sonal and Rajeev Kumar (2022) A unification of heterogeneous data sources into a graph model in e-commerce. *Data Science & Engineering* 7(1): 57-70, March 2022. Springer. <https://doi.org/10.1007/s41019-021-00174-0>.

[18] Mahesh Shirole and Rajeev Kumar (2022). Concurrent behavioral coverage criteria for sequence diagrams. *Innovations in Systems & Software Engineering* 20 pages. Springer. DOI: 10.1007/s11334-021-00413-7 (Published Nov. 25, 2021)

[19] Sonal and Rajeev Kumar (2021) Graph Model based recommendation architecture for e-commerce applications. *Infocomp Journal Computer Science* 20(2), Dec. 2021.

[20] Mahesh Shirole and Rajeev Kumar (2021). Constrained Permutation Based Test Scenario Generation from Concurrent Activity Diagrams. *Innovations in Systems & Software Engineering* 17: 343–353, Dec. 2021. Springer. DOI: 10.1007/s11334-021-00389-4 (Published April 01, 2021)

[21] Akanksha Mukhriya and Rajeev Kumar (2021) Building outlier detection ensembles by selective parameterization of heterogeneous methods. *Pattern Recognition Letters* 146C: 126 – 133, June 2021. DOI: 10.1016/j.patrec.2021.03.008

[22] Sonam Chhikara and Rajeev Kumar (2021) Image steganalysis with entropy hybridized with chaotic grasshopper optimizer. *Multimedia Tools and Applications* 80(21), 31865-31885, 2021. Springer. DOI: 10.1007/s11042-021-11118-1.

[23] Roopam Sadh and Rajeev Kumar (2021) Directional Pattern-based Clustering for Quantitative Survey Data: Method and Application. *Survey Research Methods* 15(2): 169-185, 2021. <https://doi.org/10.18148/srm/2021.v15i2.7773>

[24] Mahesh Shirole and Rajeev Kumar (2021) Concurrency coverage criteria for activity diagrams. *IET Software* 15(1): 43-54, Feb. 2021. John Wiley (OAJ). DOI: 10.1049/sfw2.12009

[25] Roopam Sadh and Rajeev Kumar (2020) Clustering of quantitative survey data based on marking patterns. *Infocomp Journal Computer Science* 19(2): 109-119, Dec. 2020.

[26] Neeraj Pathak and Rajeev Kumar (2020) Hybrid evolutionary algorithm for travelling thief problem. *Infocomp Journal Computer Science* 19(2): 132-140, Dec. 2020.

[27] Sonam Chhikara and Rajeev Kumar (2020) MI-LFGOA: Multi-island levy-flight based grasshopper optimization for spatial image steganalysis. *Multimedia Tools and Applications* 79(39): 29723-29750, Oct. 2020. Springer. DOI: 10.1007/s11042-020-09328-0

[28] Sonam Chhikara and Rajeev Kumar (2020) An Information theoretic image steganalysis for LSB steganography. *Acta Cybernetica* 24(4): 593-612, Oct. 2020. DOI: 10.14232/actacyb.279174.

[29] Priti Kumari and Rajeev Kumar (2020) Scientometric analysis of computer science publications in journals and conferences with publication patterns. *Journal of Scientometric Research* 9(1): 54-62, Jan-Apr 2020. DOI: 10.5530/jscires.9.1.6.

[30] Neha Kumari and Rajeev Kumar (2019) Evolution of generic programming in OOPs. *ACM SIGSOFT Software Engineering Notes* 44(1): 35 – 43, Jan. 2019. ACM. DOI: 10.1145/3310013.3310033

[31] Mahesh Shirole and Rajeev Kumar (2013) UML behavioral model based test case generation: A survey. *ACM SIGSOFT Software Engineering Notes* 38(4): July 2013. ACM.

[32] Soma Saha, Rajeev Kumar and Gyan Baboo (2013) Characterization of graph properties for improved Pareto fronts using heuristics and EA for bi-objective graph coloring problem. *Applied Soft Computing* 13(5): 2812 – 2822, May 2013. Elsevier.

[33] Mahesh Shirole and Rajeev Kumar (2012) Testing for concurrency in UML diagrams. *ACM SIGSOFT Software Engineering Notes* 37(5), Sep. 2012. ACM.

[34] Surender Kumar and Rajeev Kumar (2012) Precise Static Analysis for Generic Programs in Object Oriented Languages. *ACM SIGSOFT Software Engineering Notes* 37(3): May 2012. ACM.

[35] S. Harikrishnan and Rajeev Kumar (2012) Space efficient non-constant time multi-method dispatch in object oriented systems. *ACM SIGSOFT Software Engineering Notes* 37(2): Mar. 2012. ACM.

[36] Pravanjan Choudhury, PP Chakrabarti and Rajeev Kumar (2012) Online Scheduling of Dynamic Task Graphs with Communication and Contention for Multiprocessors. *IEEE Trans. Parallel and Distributed Systems* 23(1): 126 – 133, Jan. 2012.

[37] Rajeev Kumar and Nilanjan Banerjee (2011) Multiobjective network topology design. *Applied Soft Computing* 11 (8): 5120 - 5128, Dec. 2011. Elsevier.

[38] Soma Saha and Rajeev Kumar (2011) Bounded-diameter MST instances with hybridization of multi-objective EA. *J. Computer Applications* 18(4): 17 – 25, 2011.

[39] Rajeev Kumar and PK Singh (2010) Assessing solution quality of biobjective 0-1 Knapsack problem using evolutionary and heuristic algorithms. *Applied Soft Computing* 10(3): 711 - 718, June 2010. Elsevier.

[40] Dipankar Das, PP Chakrabarti, and Rajeev Kumar (2010) Thermal analysis of multiprocessor SoC applications by simulation and verification. *ACM Trans. Design Automation of Electronic Systems* (TODAES) 15(2), Article 15, Pages 52, Feb. 2010. ACM.

[41] Sandip Aine, PP Chakrabarti and Rajeev Kumar (2010) Heuristic search under contract. *Computational Intelligence* 26 (4); 386-419. Blackwell.

[42] Dipankar Das, PP Chakrabarti, and Rajeev Kumar (2009) Scenario based timing verification of multiprocessor embedded applications. *ACM Trans. Design Automation of Electronic Systems* (TODAES) 14(3), Article 37, Pages 58, May 2009. ACM.

[43] Sandip Aine, Rajeev Kumar, and PP Chakrabarti (2009) Adaptive parameter control of evolutionary algorithms to improve quality-time trade-off. *Applied Soft Computing* 9 (2): 527-540, Mar. 2009. Elsevier.

[44] SK Panda, Arnab Roy, PP Chakrabarti and Rajeev Kumar (2008) Simulation Based Verification using Temporally Attributed Boolean Logic. *ACM Trans. Design Automation of Electronic Systems* (TODAES) 13(4), Article 63, Pages 52, Sep. 2008. ACM Press.

[45] Rajeev Kumar and Dipankar Das (2008) Code compression for performance enhancement of variable length embedded processors. *ACM Trans. Embedded Computing Systems* 7(3), Article 35, Pages 36, Apr 2008. ACM Press.

[46] Vasant Patil and Rajeev Kumar (2008) A fast inverse motion compensation algorithm for DCT-domain video transcoder. *IEEE Trans. Circuits and Systems for Video Technology* 18(3): 394 – 399, Mar. 2008. IEEE Press.

[47] Pravanjan Choudhury, Rajeev Kumar and PP Chakrabarti (2008) Conditional and unpredicted task scheduling with selective duplication for embedded multiprocessors under memory and time constraints. *IEEE Trans. Parallel and Distributed Systems* 19 (7): 967 - 980, July 2008. IEEE CS Press.

[48] DP Mohapatra, M. Sahu, Rajeev Kumar, and R. Mall (2008) Dynamic slicing of aspect-oriented programs. *Informatica* 32 (3): 261 - 274, Oct. 2008. Slovene Informatika.

[49] Dipankar Das, PP Chakrabarti, and Rajeev Kumar (2007) Functional verification of task partitioning for multiprocessor embedded systems. *ACM Trans. Design Automation of Electronic Systems* 12(4), Article 44, Pages 53, Sep 2007. ACM Press.

[50] Sandip Aine, PP Chakrabarti, and Rajeev Kumar (2007) An automated meta-level control framework for optimizing the quality-time trade-off of VLSI algorithms. *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems* 26(11): 1992 – 2008, Nov 2007. IEEE Press.

[51] Rajeev Kumar (2007) "A statistical approach to robust video temporal indexing and segmentation". *Int. Journal Wavelets, Multiresolution and Information Processing* 5 (5): 769 – 783, Sep 2007. World Scientific.

[52] Rajeev Kumar and Vikram Agrawal (2007) "Multiple dispatch in reflective runtime environment". *Computer Languages, Systems & Structures* 33 (2) : 60 – 78, 2007. Elsevier.

[53] Rajeev Kumar and Soham S. Chakraborty (2007) "Precise static type analysis for object oriented programs". *ACM SIGPLAN Notices* 42 (2) : 17 – 27, Feb 2007. ACM Press.

[54] DP Mohapatra, R. Mall, and Rajeev Kumar (2007) "A parallel algorithm for dynamic slicing of distributed Java programs in non-DSM systems". *Int. J. Information & Communication Technology*, 1(1): 38 – 49, 2007.

[55] DP Mohapatra, Rajeev Kumar, R. Mall, DS Kumar, and M. Bhasin (2006) "Distributed dynamic slicing of Java programs". *Journal Systems & Software* 79 (12) : 1661 – 1678, Dec 2006. Elsevier.

[56] Rajeev Kumar and Vasant Patil (2006) "An efficient motion vector composition scheme for arbitrary frame down-sampling video transcoder." *IEEE Trans. Circuits and Systems for Video Technology* 16 (9): 1164 – 1171, Sep 2006. IEEE Press.

[57] Vasant Patil, Rajeev Kumar and Jayanta Mukherjee (2006) "A fast arbitrary factor video re-sizing algorithm". *IEEE Trans. Circuits and Systems for Video Technology* 16 (9): 1148 - 1152, Sep. 2006. IEEE Press.

[58] Arnab Sarkar, PP Chakrabarti, and Rajeev Kumar (2006) "Frame based proportional round-robin". *IEEE Trans. Computers* 55 (9) : 1121 – 1129, Sep. 2006. IEEE CS Press.

[59] Ashok Turuk and Rajeev Kumar (2006) "A flexible contention resolution scheme for QoS provisioning in optical burst switching networks". *Computer Communications* 29 (12) : 2361 – 2376, Aug 2006. Elsevier.

[60] Rajeev Kumar and Nilanjan Banerjee (2006) "Analysis of a multiobjective evolutionary algorithm on the 0-1 knapsack problem". *Theoretical Computer Science* 358 (1), 104 - 120, July 2006. Elsevier.

[61] DP Mohapatra, R. Mall, and Rajeev Kumar (2006) "An overview of slicing techniques for object-oriented programs". *Informatica* 30 (2) : 253 – 277, 2006. Slovene Informatika.

[62] Rajeev Kumar, Amit Gupta, BS Pankaj, Mrinmoy Ghosh, and PP Chakrabarti (2005) "Post-compilation optimization for multiple gains with pattern matching". *ACM SIGPLAN Notices* 40 (12): 14 – 23, Dec 2005. ACM Press.

[63] Ashok Turuk and Rajeev Kumar (2005) "QoS provisioning in WDM ring networks with tunable transceivers". *Journal of High Speed Networks* 14 (4): 317 – 339, Nov 2005. IOS Press.

[64] DP Mohapatra, R. Mall, and Rajeev Kumar (2005) "Computing dynamic slices of concurrent object-oriented programs". *Information & Software Technology* 47 (12): 805 – 817, Sep 2005. Elsevier.

[65] Ashok Turuk and Rajeev Kumar (2005) "Delay-on-Demand: A signaling protocol to reduce blocking probability in optical burst switching networks". *Photonic Network Communications*, 10 (2): 253 – 266, Sep 2005. Kluwer/Springer.

[66] Sujoy Ghosh, Rajeev Kumar, Nilanjan Banerjee, and Raja Datta (2005) "Multi-hop virtual topology design in WDM optical networks for self-similar traffic". *Photonic Network Communications*, 10 (2): 199 – 214, Sep 2005. Kluwer/Springer.

[67] Rajeev Kumar and Vishnu Makkapati (2005) "Encoding of multispectral and hyperspectral image data using wavelet transform and gain shape vector quantization". *Image & Vision Computing* 23 (8): 721 – 729, Aug 2005. Elsevier.

[68] Arnab Roy, SK Panda, Rajeev Kumar, and PP Chakrabarti (2005) "A framework for systematic validation and debugging of pipelined simulators". *ACM Trans. Design Automation of Electronic Systems*, 10 (3): 462 – 491, July 2005. ACM Press.

[69] Rajeev Kumar, Vikram Agrawal, and Anil Mangolia (2005) "Realization of multimethods in single dispatch object-oriented languages". *ACM SIGPLAN Notices* 40 (5): 18 – 27, May 2005. ACM Press.

[70] Raja Datta, Ashok Turuk, Sujoy Ghose, Rajeev Kumar, and IS Gupta (2005) "New schemes for connection establishment in GMPLS environment for WDM networks". *Int. Journal Wireless & Optical Communications*, 2005. World Scientific.

- [71] Ashok Turuk and Rajeev Kumar (2004) "A token based distributed algorithm to support QoS in a WDM ring network". *Optics Communications* 240 (1-3): 99 - 121, Oct 2004. Elsevier.
- [72] Ashok Turuk and Rajeev Kumar (2004) "A scalable and collision-free MAC protocol for all optical ring networks", *Computer Communications* 27 (15): 1453 – 63, Sep 2004. Elsevier.
- [73] AK Turuk, Rajeev Kumar and R. Badrinath (2004) "A token based distributed algorithm for medium access in an optical ring network", *Optics Communications* 231(1-6): 199 – 212, Feb 2004. Elsevier.
- [74] Rajeev Kumar and Peter Rockett (2002) "Improved sampling of the Pareto front in multiobjective genetic optimisations by steady state evolution: a Pareto Converging Genetic Algorithm", *Evolutionary Computation* 10 (3): 283 – 314, July 2002. MIT Press.
- [75] Rajeev Kumar (2001) "A neural network compiler system for hierarchical organization", *ACM SIGPLAN Notices* 36 (2): 26 – 36, Feb 2001. ACM Press.
- [76] N. Chakraborty, Rajeev Kumar, and Dilip Jain (2001) "A study of continuous casting mold using a Pareto converging genetic algorithm". *Applied Mathematical Modelling* 25 (1): 287 – 297, Jan 2001. Elsevier.
- [77] Rajeev Kumar (2000) "ANCHOR – a connectionist architecture for partitioning feature spaces and hierarchical nesting of neural nets", *Int. J. Artificial Intelligence Tools* 9 (3): 397 – 416, Sep 2000. World Scientific.
- [78] Mayur Naik and Rajeev Kumar (2000) "Efficient message dispatch in object oriented systems", *ACM SIGPLAN Notices* 35(3): 49 – 58, Mar 2000. ACM Press.
- [79] Mayur Naik and Rajeev Kumar (1999) "Object oriented symbol management in syntax directed compiler systems", *ACM SIGPLAN Notices* 34(6): 58 – 67, June 1999. ACM Press.
- [80] Rajeev Kumar and PI Rockett (1998) "Multiobjective genetic algorithm partitioning for hierarchical learning of high dimensional pattern spaces: a Learning follows Decomposition strategy", *IEEE Trans. Neural Networks* (Special issue on Hybrid Intelligent Models) 9(5): 822 – 830, Sep 1998. IEEE Press.
- [81] Rajeev Kumar and PI Rockett (1998) "Decomposition of high dimensional pattern spaces for hierarchical classification" *Kybernetika*, 34(4): 435 – 442, Sep 1998. Academy Sciences, Czech Republic.
- [82] Rajeev Kumar and PI Rockett (1997) "Triplet geometric representation: a novel scale, translation and rotation invariant feature representation based on geometric constraints for recognition of 2D object features", *Image & Vision Computing* 15(3): 235 – 249, Mar 1997. Elsevier.

Research Papers in Refereed Conference/Workshop Proceedings (incomplete list)

(Papers are grouped first in different areas and then, in reverse chronology)

Machine Learning, Data Analytics & Scientometrics::

- [83] Biraja Mishra and Rajeev Kumar (2023) Empirical analysis of variable thresholding for autoencoder anomaly detector in ECG. In Proc. 7th Int. Conf. Info. Comm. Tech. for Intelligent Systems (ICTIS). April 2023. Springer.
- [84] Law Kumar and Rajeev Kumar (2023) Community detection algorithms in social networks: an empirical evaluation. In Proc. 3rd Int. Conf. Information Technology. March 2023. Springer.
- [85] Sai Teja Tangudu and Rajeev Kumar (2023) Analysis of cost-sensitive algorithms for degree of imbalancing. In Proc. Int. Conf. Computational Intelligence in Data Science (ICCIDIS). Feb. 2023. Springer.
- [86] Bhupendra Kumar and Rajeev Kumar (2022) Difference-attribute based clustering for ordinal survey data. In Proc. 9th Int. Conf. Signal Processing & Integrated Networks (SPIN). Aug. 2022. Springer.
- [87] Farheen and Rajeev Kumar (2022) Parametrization of sequential neural networks for predicting air pollution. 3rd Proc. Int. Conf. Data Intelligence & Cognitive Informatics (ICDICI 2022). July 2022. Springer.
- [88] Bhupendra Kumar and Rajeev Kumar (2022) Entropy based clustering for subspace pattern discovery in ordinal survey data. In Proc. 10th Int. Conf. Frontiers of Intelligent Computing: Theory and Applications (FICTA). June 2022. Springer.

[89] Gargi Mishra and Rajeev Kumar (2023) Group fairness in outlier detection ensembles. In Proc. Int. Conf. Computer Vision & Robotics (CVR). *Algorithms for Intelligent Systems*. May 2022. Springer. *{Best Paper Award in Emerging Technology}* https://doi.org/10.1007/978-981-19-7892-0_39

[90] Anish Sharma and Rajeev Kumar (2022) Imbalanced learning of regular grammar for DFA extraction from LSTM architecture. In Proc. 11th Int. Conf. Soft Computing for Problem Solving (SocProS). May 2022. Springer.

[91] Pooja Singh and Rajeev Kumar (2022) Assessing imbalanced datasets in binary classifiers. In Proc. 11th Int. Conf. Soft Computing for Problem Solving (SocProS). May 2022. Springer.

[92] Trishita Mukherjee and Rajeev Kumar (2022) Localized community-based node anomalies in complex networks. In Proc. 11th Int. Conf. Soft Computing for Problem Solving (SocProS). May 2022. Springer

[93] Junaciya K, Akhilesh Rawat, and Rajeev Kumar (2022) Performance assessment of normalization in CNN with retinal image segmentation. In Proc. 11th Int. Conf. Soft Computing for Problem Solving (SocProS). May 2022. Springer

[94] Farheen and Rajeev Kumar (2022) Correlated features in air pollution prediction. In Proc. Int. Conf. Artificial Intelligence: Advances and Applications (ICAIAA). April 2022. Springer.

[95] Akhilesh Rawat and Rajeev Kumar (2022) Assessing layer normalization with BraTS MRI data in a CNN. In proc. Int. Conf. Computational Intelligence in Data Science (ICCIDIS), vol. IFIP AICT 654, pp. 124-135, March 2022, Springer Nature, Switzerland. https://doi.org/10.1007/978-3-031-16364-7_10

[96] Gournga Duari and Rajeev Kumar (2022) Hierarchical learning of outliers. In Proc. 5th Int. Conf. Communications & Cyber-Physical Engineering (ICCCE). April 2022. Springer.

[97] Gournga Duari and Rajeev Kumar (2022) Clustering for global and local outliers. In Proc. 4th Int. Conf. Machine Intelligence and Signal Processing (MISP). March 2022. Springer.

[98] Om Prakash and Rajeev Kumar (2021) Fake account detection in social networks with supervised learning. In Proc. Int. Conf. Intelligent Computing & Security (IICS). Springer.

[99] Om Prakash and Rajeev Kumar (2022) Fake news detection in social networks using attention mechanism. In Proc. Int. Conf. Cognitive & Intelligent Computing (ICCIC). Springer Nature. https://doi.org/10.1007/978-981-19-2358-6_42

[100] Priti Kumari and Rajeev Kumar (2021) Scientometrics and publications: a comparative study of ranking of multisource databases. In Proc. Int. Conf. Data Science, Machine Learning & Applications (ICDSMLA). Springer

[101] Rajeev Kumar (2020) Academic Autonomy in HEIs: The Most Used, Misused yet the Least Understood Term? Int. Summit in Quality Indices in Higher Edu., DTU Delhi, Nov 2020.

[102] Priti Kumari, Roopam Sadh, and Rajeev Kumar (2020) Research criteria for measuring quality: Do they promote genuine research or manipulation? Int. Summit in Quality Indices in Higher Edu., DTU Delhi, Nov 2020.

[103] Roopam Sadh and Rajeev Kumar (2020) Quality Indicators of HEIs: Are they adequate in Indian Context. Int. Summit in Quality Indices in Higher Edu., DTU Delhi, Nov 2020.

[104] Akanksha Mukhriya and Rajeev Kumar (2020) Homogeneous pools to heterogeneous ensembles for unsupervised outlier detection. In: Proc. Information, Communication & Computing Technology (ICICCT). Communications in Computer & Information Science, vol. 1170, 2020. Springer.

[105] Neha Kumari and Rajeev Kumar (2020) Profiling JVM for AI applications using deep learning libraries. In: Proc. ICTIS 2020. Springer.

[106] Roopam Sadh and Rajeev Kumar (2020) Clustering of Quantitative Survey Data: A Subsystem of EDM Framework. In Proc. Int Conf. Computational Methods & Data Engineering (ICDME). Advances in Intelligent Systems and Computing. Springer.

[107] Roopam Sadh and Rajeev Kumar (2019) EDM framework for knowledge discovery in educational domain. In Recent Trends in Communication, Computing, and Electronics, pages 409–417. Springer.

[108] Akanksha Mukhriya and Rajeev Kumar (2018) Exploring Ensembles for Unsupervised Outlier Detection: An Empirical Analysis. TEAMC, NSIT Delhi, March 2018. Springer.

[109] Sandip Aine, P. P. Chakrabarti, Rajeev Kumar (2010) Contract Search: Heuristic Search under Node Expansion Constraints. In Proc. Euro. Conf. AI (ECAI), Lisbon, Portugal, pp. 733-738. IOS Press.

[110] Sandip Aine, PP Chakrabarti, and Rajeev Kumar (2009) Contract Search: An adaptive heuristic search strategy under node expansion constraints, In Symposium on Combinatorial Search (SoCS-09), Los Angles. AAAI Inc.

[111] Sandip Aine, PP Chakrabarti, and Rajeev Kumar (2007) AWA* - A window constrained anytime heuristic search algorithm. In Proc. 12th Int. Jt. Conf. Artificial Intelligence (IJCAI-07), Hyderabad, pp. 2250 - 2255, January 2007. IJCAI, Inc.

[112] Sandip Aine, Rajeev Kumar, and PP Chakrabarti (2005) "An adaptive framework for solving multiple hard problems under time constraints". In Proc. Int. Conf. Computational Intelligence and Security (CIS-05), Xi'an, China, LNCS 3801: 57 - 64, Dec 2005. Springer.

[113] Sandip Aine, Rajeev Kumar, and PP Chakrabarti (2005) "Adaptive control of anytime algorithm parameters". In Proc. 2nd Indian Int. Conf. Artificial Intelligence (IICAI-05), Pune, pp. 72 - 87, December 2005.

[114] Rajeev Kumar (2003) "Scaling and generalisation in data-Mining by meta-learning of data-partitions". In Proc. Int. Conf. Info. Tech. - Prospects and Challenges (ITPC-03), vol. 2: 27 - 34, Kathmandu, 23- 26 May 2003.

[115] Rajeev Kumar and PI Rockett (2002) "A bootstrapped modular learning approach for scaling and generalization of grey-level corner detection". In Proc. Advances in Soft Computing, LNCS (Subseries LNAI), 2275: 395 - 400, Feb 2002. Springer.

[116] Rajeev Kumar (2000) A grey-level image corner detector using a modular neural network, In 2nd Indian Conference Computer Vision, Graphics & Image Processing (Icvgip-00), Bangalore, 20-22 December 2000.

[117] Rajeev Kumar (1999) "On generalization of machine learning with neural-evolutionary computations". 3rd Int. Conf. Computational Intelligence & Multimedia Applications (ICCIMA-99), New Delhi, pp. 112-116, September 1999. IEEE CS Press.

[118] Rajeev Kumar (1998) "A connectionist architecture for scaling neural computation". Int. Symp. Intelligent Robotic Systems (ISIRS-98), Bangalore, pp. 215-218, 10-12 January 1998.

[119] Rajeev Kumar (1997) "Superneuron: A generalisation of neuron for partitioning and nesting in modular neural systems". 6th IEEE Regional Symp. Intelligent Systems, Bangalore, pp. 150-154, 20-21 November 1997.

[120] Rajeev Kumar, WC Chen, and PI Rockett (1997) "Bayesian labelling of image corner features using a grey-level corner model with a bootstrapped modular neural network". IEE 5th Int. Conf. Artificial Neural Networks (ANN-97), Cambridge UK, pp. 82-87, 7-9 July 1997. IEE Conference Publication No. 440.

[121] Rajeev Kumar and PI Rockett (1997) "Decomposition of high dimensional pattern spaces for hierarchical classification". IAPR Workshop Statistical Techniques in Pattern Recognition (STIPR-97), Institute of Information Theory & Automation, Academy of Sciences of Czech Republic. Prague Czech Republic, pp. 97-102, 9-11 June 1997.

[122] Rajeev Kumar and PI Rockett (1996) "ANCHOR - A connectionist architecture for hierarchical nesting of multiple heterogeneous neural nets". AAAI Workshop Integrating Multiple Learned Models (IMLM 96), Portland Oregon USA, pp. 59-65, 4-5 August 1996. Menlo Park, Calif.: AAAI Press.

Programming Languages & Software Systems::

[123] Mahesh Shirole, Amit Suthar, and Rajeev Kumar (2022) Generation of improved test cases from UML state diagram using genetic algorithm. *Test of Time Award* presentation in 15th Innovations in Software Engineering Conf. (ISEC) as the most impactful paper from amongst the published paper 10 (+-1) years ago in ISECs, Feb. 26, 2022. ACM *iSoft/SigSoft*.

[124] Neha Kumari and Rajeev Kumar (2021) Type Inference in Java: Characteristics and Limitations. In Proc. ICCMLA 2020: *Cybernetics, Cognition, & Machine Learning Applications*, Algorithms for Intelligent Systems Series: 131-138. Springer.

[125] Sonal Tuteja and Rajeev Kumar (2018) An Architecture for Data Unification in E-commerce using Graph. 9th Int. Conf. Quality, Reliability, Infocom Tech & Business Operations (ICQRIT), Dec 2018.

[126] Sonal Tuteja and Rajeev Kumar (2017) A System Architecture for Mapping Application Data into Complex Graph. In Proc. Information, Communication and Computing Technology (ICICCT). *Communications in Computer and Information Science*, vol. 750: pp. 148 – 155. Springer.

[127] Mahesh Shirole and Rajeev Kumar (2015) Test scenario selection for concurrency testing from UML models. In Proc. Int. Conf. Contemporary Computing, pp. 531-536. IEEE Press.

[128] Mahesh Shirole, Mounika Kommuri, and Rajeev Kumar (2012) Transition sequence exploration of UML activity diagram using evolutionary algorithm. In Proc. India Software Engineering Conf., pp. 97 – 100. ACM.

[129] Mahesh Shirole, Amit Suthar, and Rajeev Kumar (2011) Generation of improved test cases from UML state diagram using genetic algorithm. In Proc. India Software Engineering Conf., pp. 125-134. ACM.

[130] Mahesh Shirole and Rajeev Kumar (2010) A hybrid genetic algorithm based test case generation using sequence diagrams. In Proc. Int. Conf. Contemporary Computing, pp. 53-63, 2010. Springer.

[131] Abhiram Kasina, Amit Suthar and Rajeev Kumar (2010) Detection of polymorphic viruses in windows executables. In Proc. Int. Conf. Contemporary Computing, pp. 120-130, 2010. Springer.

[132] Pranith Kumar D., Anchal Nema and Rajeev Kumar (2009) Hybrid analysis of executables to detect security vulnerabilities. In Proc. 3rd Hackers' Workshop, Kanpur, pp. 9 - 16, March 2009. Also, in Proc. 2nd India Software Engineering Conference (ISEC), Pune, pp. 141 - 142, February 2009. ACM.

[133] Soham S. Chakraborty and Rajeev Kumar (2008) Precise static type analysis in component based programming environment. In Proc. 1st India Software Engineering Conference (ISEC), Hyderabad, pp. 133 - 134, February 2008. ACM.

[134] Soham S. Chakraborty and Rajeev Kumar (2007) Static analysis based application specific dispatch table compaction. In Proc. 15th Int. Conf. Advance Computing and Communication (ADCOM), Guwahati, December 2007. IEEE CS Press

[135] Avik Paul and Rajeev Kumar (2007) Precise dynamic slicing using execution summary. In Proc. 22nd Annual ACM Symposium on Applied Computing (SAC-07) (Programming Languages Track), Seoul, Korea, pp. 1330 - 1331, March 2007. ACM

[136] SS Chakraborty and Rajeev Kumar. Prioritizing methods for optimal method inlining. In Web Proc. 13th Int. Conf. High Performance Computing Conference (HiPC), Bangalore, December 2005.

[137] Dipankar Das, Rajeev Kumar, and PP Chakrabarti (2006) "Timing verification of UML activity diagram based code block level models for real-time multiprocessor system-on-chip Applications". In Proc. 13th Asia Pacific Software Engineering Conference (APSECo6), Bangalore, pp. 199 - 206, December 2006. IEEE CS Press.

[138] Rajeev Kumar, Rahul Chaudhry, Dipankar Das, Vibha Rathi, S.K. Panda, and P.P. Chakrabarti (2006) "SystemC Modeling and Validation of a Pipelined RISC Processor Based System". In Proc. Forum of Specification & Design Languages (FDL-06), Darmstadt, Germany, pp. 189 – 196, September 2006.

[139] Anshuman Mishra, Rajeev Kumar, and PP Chakrabarti (2005) "A method-based whole-program watermarking scheme for Java class files". In Web Proc. 12th Int. Conf. High Performance Computing Conference (HiPC), Goa, December 2005. [Best Poster Award]

[140] DP Mohapatra, R. Mall, and Rajeev Kumar (2005) "A parallel algorithm for dynamic Slicing of distributed Java programs in non-DSM systems". In Proc. 8th Int. Conf. Information Technology (CiT), Bhubaneswar, pp 3 - 6, December 2005.

[141] DP Mohapatra, R. Mall, and Rajeev Kumar (2004) "A novel method for computing dynamic slices of object-oriented programs with conditional statements". In Proc. IEEE India Council Conference (Indicon-04), Kharagpur. December 2004. Available online at IEEE Digital Library.

[142] DP Mohapatra, R. Mall, and Rajeev Kumar (2004) "A novel method for computing dynamic slices of concurrent C++ program". In Proc. 12th Int. Conf. Advanced Computing & Communications (ADCOM-04), Ahmedabad, December 2004.

[143] DP Mohapatra, R. Mall, and Rajeev Kumar (2004) "A novel approach for dynamic slicing of distributed object-oriented programs". In Proc. 1st Int. Conf. Distributed Computing & Internet Technology (IcDCIT), Bhubaneswar. LNCS 3347: 304 - 309. December 2004. Springer.

[144] DP Mohapatra, R. Mall, and Rajeev Kumar (2004) "An efficient technique for dynamic slicing of concurrent Java programs". In Proc. Asian Applied Computing Conference (AACC), Kathmandu. LNCS 3285: 255 - 262, October 2004. Springer.

[145] DP Mohapatra, R. Mall, and Rajeev Kumar. (2004) "An edge marking technique for dynamic slicing of object-oriented programs. In Proc. Int. Computer Software & Applications Conf. (CompSAC), Hongkong. Design and Assessment of Trustworthy Software-Based Systems, 60 - 65, September 2004. IEEE CS Press.

[146] S. Pankaj, Amit Gupta, Rajeev Kumar, and P. P. Chakrabarti. Optimizing binaries for multiple gain factors using state-based model. In Web Proc. *Performance Issues in Mobile Devices Workshop*, Co-located with 11th Int. High Performance Computing Conf. Bangalore. 2004.

[147] M Ghosh, Rajeev Kumar, and PP Chakrabarti. FSM Matchers: A Post Compilation Optimization technique for Extensible Architectures. In Web Proc. Int. High Performance Computing Conference. Bangalore. 2004

[148] DP Mohapatra, R. Mall, and Rajeev Kumar (2003) "Dynamic slicing of object-oriented programs". In Proc. Eleventh Int. Conf. Advanced Computing & Communications (ADCOM-03), Coimbatore, pp. 1 - 14, Dec 2003.

[149] DP Mohapatra, R. Mall, and Rajeev Kumar (2003) "A novel approach for slicing of object-oriented programs". In Proc. Sixth Int. Conf. Information Technology (CiT 2003), Bhubaneswar, pp. 110 - 115, December 2003.

[150] DP Mohapatra, R. Mall, and Rajeev Kumar (2003) "Dynamic slicing of object-oriented programs". In Proc. Int. Conf. Info. Tech. - Prospects and Challenges (ITPC-03), pages 283 - 290, Kathmandu, 23- 26 May 2003.

[151] Rajeev Kumar, N. Vijay Kumar, and IJ Nagrath (1999) "Object oriented toolkit for multiobjective genetic optimization". 3rd Int. Conf. Computational Intelligence & Multimedia Applications (ICCIMA-99), New Delhi, pp. 96-100, September 1999. IEEE CS Press.

Multiobjective Combinatorial Optimization & Evolutionary Algorithm ::

[152] Neeraj Pathak and Rajeev Kumar (2019) Improved Wisdom of Crowds Heuristic for Solving Sudoku Puzzles. In Proc. Soft Computing and Signal Processing. Conf. Advances in Intelligent Systems & Computing, vol 900, pp. 369-377. Springer, Singapore. https://doi.org/10.1007/978-981-13-3600-3_34

[153] Neeraj Pathak and Rajeev Kumar (2017) A hybridized evolutionary algorithm for bi-objective bi-dimensional bin-packing problem. In Proc. Information, Communication and Computing Technology. ICICCT. Communications in Computer and Information Science, vol. 750: pp. 296 – 304. Springer.

[154] Soma Saha, Gyan Baboo, Rajeev Kumar (2011) An Efficient EA with Multipoint Guided Crossover for Bi-objective Graph Coloring Problem. In Proc. 4th Int. Conf. Contemporary Computing (IC3): Noida, pp. 135 - 145, August 2011. Communications in Computer and Information Science (CCIS) 168, 2011. Springer.

[155] Soma Saha and Rajeev Kumar (2011) Improvement of bounded-diameter MST instances with hybridization of multi-Objective EA (2011) In Proc. Int. Conf. Comm. Comput. Sec. (ICCCS), Rourkela, February 2011. ACM.

[156] Soma Saha, Mohammad Aslam and Rajeev Kumar (2010) Assessing the Performance of Bi-objective MST for Euclidean and Non-Euclidean Instances. In Proc Int. Conf. Contemporary Computing, pp. 229-240, 2010. Springer.

[157] Rajeev Kumar, Bipul K. Bal and Peter Rockett (2009) Multiobjective genetic programming approach to evolving heuristics for the bounded diameter minimum spanning tree problem. In Proc. Genetic and Evolutionary Computation Conference (GECCO-2009), Montréal, pp. 309 – 316, July 2009. ACM.

[158] Paresh Tolay and Rajeev Kumar (2009) "Evolution of hyperheuristics for the biobjective graph coloring problem using multiobjective genetic programming". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2009), Montréal, pp. 1939-1940, July 2009. ACM.

[159] Rajeev Kumar, Ashwin Joshi, Krishna Banka and Peter Rockett (2008) "Evolution of hyperheuristics for biobjective 0/1 knapsack problem by multiobjective genetic programming". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2008), Atlanta, pp. 1227 – 1234, July 2008. ACM

[160] Rajeev Kumar, Paresh Tolay and Siddharth Tiwary (2008) "Enhancing solution quality of the biobjective graph coloring problem using hybridization of EA". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2008), Atlanta, pp. 547 – 554, July 2008. ACM.

[161] Nilanjan Banerjee and Rajeev Kumar (2007) "Multiobjective network design for realistic traffic models". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2007), London, pp. 1904 - 1911, July 2007. ACM. [Best paper nominee]

[162] Rajeev Kumar and PK Singh (2007) "On quality performance of heuristic and evolutionary algorithms for biobjective minimum spanning trees". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2007), London, pg. 2259, July 2007. ACM.

[163] Rajeev Kumar and PK Singh (2007) "Evolutionary local search for biobjective intersecting spanning trees from geometric graphs". In LBP Proc. Fourth Int. Conf. Evolutionary Multi-Criterion Optimization (EMO), Matsushima/Sendai, Japan, pp. 1 - 6, March 2007.

[164] Rajeev Kumar, PK Singh, and Bhargab B Bhattacharya (2006) "Biobjective evolutionary and heuristic algorithms for intersection of geometric graphs". In Proc. Genetic and Evolutionary Computation Conference (GECCO-2006), Seattle, USA, pp. 1689 – 96, July 2006. ACM.

[165] Rajeev Kumar, PK Singh, AP Singhal, and Atul Bhartia (2006) "Evolutionary and heuristic algorithms for multiobjective 0-1 knapsack problem". In Proc. 10th Online World Conf. Soft Computing in Industrial Applications (WSC10), September/October 2005. In A. Tiwari, J. Knowles, E. Avineri, K. Dahal, and R. Roy (Eds.), Applications of Soft Computing: Recent Trends, May 2006. ISBN 3-540-291-23-7. Springer.

[166] Sandip Aine, Rajeev Kumar, and PP Chakrabarti (2006) "Adaptive parameter control of evolutionary algorithms under time constraints". In Proc. 10th Online World Conf. Soft Computing in Industrial Applications (WSC10), September/October 2005. In A. Tiwari, J. Knowles, E. Avineri, K. Dahal, and R. Roy (Eds.), Applications of Soft Computing: Recent Trends, May 2006. ISBN 3-540-291-23-7. Springer.

[167] Rajeev Kumar and Nilanjan Banerjee (2005) "Running time analysis of a multiobjective evolutionary algorithm on simple and hard problems". In Proc. Foundations of Genetic Algorithms (FoGA) Workshop, AizuWakamatsu, Japan, January 2005. LNCS 3469: 112 - 131, March 2005. Springer.

[168] Rajeev Kumar, PK Singh and PP Chakrabarti (2005) "Multiobjective EA approach for improved quality of solutions for spanning tree problem". In Proc. 3rd Int. Conf. Evolutionary Multi-Criterion Optimization (EMO-05), Guanajuato, Mexico. LNCS 3410: 811- 825, March 2005. Springer.

[169] Rajeev Kumar, PK Singh, and PP Chakrabarti (2004) "Improved quality of solutions for multiobjective spanning tree problem using evolutionary algorithm". In Proc. 11th Int. Conf. High Performance Computing (HiPC), Bangalore. LNCS 3296: 494 - 503, December 2004. Springer.

[170] Rajeev Kumar, PK Singh, and PP Chakrabarti (2004) "Distributed evolutionary algorithm search for multiobjective spanning tree problem". In Proc. 6th Int. Workshop Distributed Computing (IWDC), Kolkata. LNCS 3326: 538, December 2004. Springer.

[171] Rajeev Kumar, PK Singh, and PP Chakrabarti (2004) "Multiobjective genetic search for spanning tree problem". In Proc. 11th Int. Conf. Neural Information Processing (Iconip), Kolkata. LNCS 3316: 218 - 223, November 2004. Springer.

[172] Nilanjan Banerjee and Rajeev Kumar (2004) "Expected running time analysis of a multiobjective evolutionary algorithm on pseudo-boolean function". In Proc. 11th Int. Conf. Neural Information Processing (Iconip), Kolkata. LNCS 3316: 193 - 198, November 2004. Springer.

[173] Rajeev Kumar and PI Rockett (2004) "Effective evolutionary multimodal optimization by multiobjective reformulation without explicit niching/sharing". In Proc. Asian Applied Computing Conference (AACC), Kathmandu. LNCS 3285: 1 - 8, October 2004. Springer.

[174] Rajeev Kumar (2003) "Multicriteria network design using distributed evolutionary algorithm". In Proc. Int. Conf. High Performance Computing (HiPC), Hyderabad. LNCS 2913: 343 - 352, December 2003. Springer.

[175] Rajeev Kumar and Nilanjan Banerjee (2003) "Multicriteria network design using evolutionary algorithm". In Proc. Genetic and Evolutionary Computing Conference (GECCO-03), Chicago, IL. LNCS 2723: 2179 - 2190, July 2003. Springer.

[176] Rajeev Kumar and PI Rockett (2003) "Evolutionary multimodal optimization revisited". In Proc. Genetic and Evolutionary Computing Conference (GECCO-03), Chicago, IL. LNCS 2723: 1592 - 1593, July 2003. Springer.

[177] Rajeev Kumar, PP Parida, and M. Gupta (2002) "Topological design of communication networks using multi-objective genetic optimization". In Proc. Congress Evolutionary Computation (CEC-2002), pages 425 - 430, May 2002. IEEE Press.

[178] Rajeev Kumar (2000) "Codebook design for vector quantisation using multiobjective genetic algorithms". PPSN/SAB Workshop Multiobjective Problem Solving from Nature (MPSN), College de France, Paris, 16 September 2000.

[179] Rajeev Kumar, S. Prasanth, and MS Sudarshan (2000) "Topological design of mesh communication networks using multiobjective genetic optimisation". PPSN/SAB Workshop Multiobjective Problem Solving from Nature (MPSN), College de France, Paris, 16 September 2000.

[180] Rajeev Kumar, VP Krishnan, and SK Santhanakrishnan (2000) "Design of an optimal communication network using multiobjective genetic optimisation". IEEE Int. Conf. Industrial Technology (ICIT-2000), 19-22 January 2000, Goa, pp. 515-520. IEEE Catalog Number 00TH 8482.

[181] Rajeev Kumar, N. Vijay Kumar, and IJ Nagrath (1999) "Object oriented toolkit for multiobjective genetic optimization". 3rd Int. Conf. Computational Intelligence & Multimedia Applications (ICCIMA-99), New Delhi, pp. 96-100, September 1999. IEEE CS Press.

[182] Rajeev Kumar and PI Rockett (1997) "Assessing the convergence of rank-based multiobjective genetic algorithms". IEE/ IEEE 2nd Int. Conf. Genetic Algorithms in Engineering Systems: Innovations & Applications (GALESIA-97), Glasgow UK, pp. 19-23, 2-4 September 1997. IEE Conference Publication No. 446.

[183] Rajeev Kumar and PI Rockett (1997) "Multiobjective genetic algorithm partitioning for hierarchical learning of high dimensional spaces". IEE Colloquium Pattern Recognition, London UK, pp. 6/1-6/6, 26 February 1997. IEE Publication Ref. No. 1997/018.

Multimedia Systems & Image Cryptanalysis ::

[184] Sonam Chhikara and Rajeev Kumar (2019) An adaptive frequency based steganography technique. In Proc. Int. Conf. Information, Communication and Computing (ICICC), May 2018. *Communications in Computer and Information Science*, vol. 839, pp 139-149. Springer. DOI: 10.1007/978-981-13-5992-7_12

[185] Sonam Chhikara and Rajeev Kumar (2018) An information theoretic steganalysis for object based LSB steganography. Fourth Int. Conf. Next Generation Computing Technology (NGCT), Dehradun, Nov. 2018.

[186] Vasant Patil and Rajeev Kumar (2007) Compressed domain inverse motion compensation in H.264/AVC video. In Proc. 10th Int. Symp. Wireless Personal Multimedia Communications (WPMC-07), Jaipur. December 2007. IEEE Press

[187] Vasant Patil and Rajeev Kumar (2007) A fast arbitrary factor H.264/AVC video re-sizing algorithm. In Proc. IEEE Int. Conf. Image Processing (ICIP-07), San Antonio, Texas, USA. September 2007. IEEE Press.

[188] Vasant Patil and Rajeev Kumar (2007) An effective motion re-estimation in frame-skipping video transcoding. In Proc. Int. Conf. Computing: Theory and Applications (ICCTA) :: Platinum Jubilee of the Indian Statistical Institute, Kolkata, India. March 2007. IEEE CS Press

[189] Vasant Patil, Tummala Kalyani, Atul Bhartia, Rajeev Kumar and Jayanta Mukherjee (2006) "DCT domain transcoding of H.264/AVC video". In Proc. 5th Indian Conference on Computer Vision, Graphics and Image Processing (ICVGIP-06), Madurai, India. LNCS 4338: 696 - 707, December 2006. Springer.

[190] Vasant Patil, Rajeev Kumar, Jayanta Mukherjee, and SS Prasad (2006) "A fast arbitrary down-sampling algorithm for video transcoding". In Proc. IEEE Int. Conf. Image Processing (ICIP-06), Atlanta, GA, USA. October 2006. IEEE Press.

- [191] Vasant Patil and Rajeev Kumar (2005) "A DCT domain frame skipping video transcoder". In Proc. IEEE Int. Conf. Image Processing (ICIP-05), Genova, Italy. September 2005. IEEE Press.
- [192] Vasant Patil and Rajeev Kumar (2005) "An arbitrary frame-skipping video transcoder". In Proc. IEEE Int. Conf. Multimedia and Expo (ICME-05), Amsterdam, The Netherlands. July 2005. IEEE Press.
- [193] Vasant Patil and Rajeev Kumar (2004) "A generic video transcoder for MPEG streams by arbitrary frame dropping". In Proc. IEEE India Council Conference (Indicon-04), Kharagpur. December 2004. Available online at IEEE Digital Library.
- [194] Ashok Turuk and Rajeev Kumar (2004) "A novel scheme to reduce burst-loss and provide QoS in optical burst switching networks". In Proc. 11th Int. Conf. High Performance Computing (HiPC), Bangalore. LNCS 3296: 309 - 318, December 2004. Springer.
- [195] Ashok Turuk and Rajeev Kumar (2004) "A distributed contention resolution scheme to reduce blocking probability in optical burst-switching networks". In Proc. 6th Int. Workshop Distributed Computing (IWDC), Kolkata. LNCS 3326: 361 - 372, December 2004. Springer.
- [196] Ashok Turuk, Rajeev Kumar, and R. Badrinath (2003) "A token based distributed algorithm for medium access in an optical ring". In Proc. Int. Workshop Distributed Computing (IWDC), Kolkata. LNCS 2918: 340 - 349, December 2003. Springer.
- [197] Rajeev Kumar (2003) "A rate adaptation transcoding to support QoS over internet for multimedia traffic". In Proc. IEEE Region 10 Conference on Convergent Technologies (Tencon 2003), Bangalore, pp. 313 - 318, October 2003. Available online at IEEE Digital Library.
- [198] Rajeev Kumar (2003) "A protocol with transcoding to support QoS over internet for multimedia traffic". In Proc. IEEE Int. Conf. Multimedia and Expo (ICME-03), Baltimore, MD. I.465 - I.468, July 2003. IEEE Press.
- [199] Rajeev Kumar and V. Devatha (2002) "Statistical approach to robust video temporal segmentation". In Proc. 3rd Indian Conference Computer Vision, Graphics & Image Processing (Icvgip-02), pages 91 - 96, 16 - 18 December 2002.
- [200] V. Makkapati and Rajeev Kumar (2002) "Improved encoding of wavelet coefficients extracted from multispectral and hyperspectral image data". In Proc. 3rd Indian Conference Computer Vision, Graphics & Image Processing (Icvgip-02), pages 191 - 196, 16 - 18 December 2002.
- [201] Rajeev Kumar, JS Rao, S. Chattopadhyay, and GK Rao (2002) "A protocol to support QoS for multimedia traffic over Internet with transcoding", HiPC Trusted Internet Workshop, Co-located with Int. Conf. High Performance Computing (HiPC), Bangalore, 18 December 2002.
- [202] Rajeev Kumar, Mayank Gupta, and Bhanu Prakash (2000) A hybrid learning algorithm for vector quantisation design, IEEE Regional Int. Conf. Control, Communication & Signal Processing (CCSP), Bangalore, 25-28 July 2000.
- [203] Rajeev Kumar (1998) Propagating errors into feature representation for robustness of local invariants. Indian Conf. Computer Vision, Graphics & Image Processing (Icvgip-98), New Delhi, pp.159-165, December 98.

Embedded Systems ::

- [204] SK Panda, VG Kasturi, PP Chakrabarti, and Rajeev Kumar (2007) Scenario driven test case generation for functional verification of pipelined processors. In Proc. 11th IEEE VLSI Design and Test Symposium (VDAT), Kolkata, India, August 2007.
- [205] Rajeev Kumar, PK Singh, and Bhargab B. Bhattacharya (2007) A local search heuristic for bijective intersecting geometric graphs. In Proc. Int. Conf. Computing: Theory and Applications (ICCTA):: Platinum Jubilee of Indian Statistical Institute, Kolkata, India. March 2007. IEEE CS Press.
- [206] SK Panda, Arnab Roy, PP Chakrabarti, and Rajeev Kumar (2007) Simulation based verification using temporally attributed boolean logic. In Proc. 20th Int. Conf. VLSI Design/ 6th Int. Conf. Embedded System, Bangalore, Jan. 2007. IEEE CS Press.

- [207] Pravanjan Choudhury, PP Chakrabarti, and Rajeev Kumar (2007) Online dynamic voltage scaling analysis using task graph mapping for multiprocessors. In Proc. 20th Int. Conf. VLSI Design/ 6th Int. Conf. Embedded System, Bangalore, Jan. 2007. IEEE CS Press.
- [208] Sandip Aine, Rajeev Kumar, and PP Chakrabarti (2006) Improving standard cell placement through adaptive parameter control. In Proc. IEEE Int. Conf. Industrial Technology (ICIT 2006), Mumbai, December, 2006.
- [209] Sandip Aine, PP Chakrabarti, and Rajeev Kumar (2006) Improving the performance of CAD optimization algorithms using on-line meta-level control. In Proc. 19th Int. Conf. VLSI Design/ 5th Int. Conf. Embedded System, Hyderabad, pp. 683 - 688, January 2006. IEEE CS Press.
- [210] Arnab Sarkar, PP Chakrabarti, and Rajeev Kumar (2006) Frame based fair multiprocessor scheduler: a fast fair algorithm for real-time embedded systems. In Proc. 19th Int. Conf. VLSI Design/ 5th Int. Conf. Embedded System, Hyderabad, pp. 677 - 682, January 2006. IEEE CS Press.
- [211] Arnab Sarkar, PP Chakrabarti, and Rajeev Kumar (2005) "Boundary fair round-robin: a fast fair scheduler". In Proc. 9th VLSI Design & Test Symp. (VDAT), Bangalore, pp. 81 - 91, August 2005. Elite Publishing.
- [212] Sanjay Chatterjee, PP Chakrabarti, and Rajeev Kumar (2005) "An optimal algorithm for register renaming: a post compilation technique". In Proc. 9th VLSI Design & Test Symp. (VDAT), Bangalore, pp. 102 - 111, August 2005. Elite Publishing.
- [213] Dipankar Das, Rajeev Kumar, and PP Chakrabarti (2005) "Dictionary based code compression for variable length instruction encodings". In Proc. 18th Int. Conf. VLSI Design/ 4th Int. Conf. Embedded System, Kolkata, pp. 545 - 550, January 2005. IEEE CS Press.
- [214] Dipankar Das, SK Panda, Rajeev Kumar, and PP Chakrabarti (2004) "SystemC modeling of a pipelined RISC processor based system". In Web Proc. Performance Issues in Mobile Devices Workshop, Co-located with 11th Int. Conf. High Performance Computing Conf. (HiPC), Bangalore, December 2004.
- [215] Dipankar Das, Rajeev Kumar, and PP Chakrabarti (2004) "Code compression using unused encoding space for variable length instruction encodings". In Proc. 8th VLSI Design & Test Workshop (VDAT), Mysore, August 2004.

Miscellaneous ::

- [216] M. Prashant, R. Siddharth, and Rajeev Kumar (1999) "Formulation of an encryption algorithm on the basis of molecular genetics and image patterns". 3rd Int. Conf. Computational Intelligence & Multimedia Applications (ICCIIMA-99), New Delhi, pp. 76-80, September 1999. IEEE CS Press.
- [217] MC Agarwal, KL Arvind, Rajeev Kumar, and IJ Nagrath (1999) "Vibratory tactile display - a fractal brownian approach". 3rd Int. Conf. Computational Intelligence & Multimedia Applications (ICCIIMA-99), New Delhi, pp. 442-446, September 1999. IEEE CS Press.
- [218] MC Agarwal, KL Arvind, Rajeev Kumar, and IJ Nagrath (1998) "Dimension estimation of image based textures for a vibratory tactile display using a fractal brownian model". Int. Conf. Knowledge Based Computer Systems (KBCS-98), Mumbai, pp.333-344, December 1998.

Doctoral Thesis Supervision:

In progress @ JNU

- [1] Priti Kumari. Scientometrics of CS publications. (by May 2023)
- [2] Neha Kumari. A safe generic type system for Java. (by Dec. 2023)
- [3] Akhilesh Rawat. Assessing generalization in deep learning architecture with medical image analysis. (by Dec. 2023)
- [4] Om Prakash. Machine intelligence with multi-modal media for social welfare. (by Dec. 2023)
- [5] Gouranga Duari. Hierarchical clustering of outliers. (by March 2024)
- [6] Bhupendra Kumar. Pattern discovery & clustering in ordinal survey data: algorithms and applications. (by March 2024)
- [7] Farheen. Deep learning for sequential data: architecture, algorithms, & applications.
- [8] Irfan Zaboo. FSA Extraction from Sequential Neural Networks.

Graduated / Submitted @ JNU

- [9] Akanksha Mukhriya. Ensemble learning for unsupervised outlier detection: algorithms and applications. Oct. 2022.
- [10] Neeraj Pathak. Hybrid evolutionary algorithms for solving hard combinatorial problems. Aug. 2022.
- [11] Sonal Tuteja. Graph model for schema and data mapping. July 2022.
- [12] Roopam Sadh. Knowledge discovery from quantitative survey data with pattern clustering and machine learning. March 2022.
- [13] Sonam Chhikara. Information theoretic image steganalysis for LSB steganography. (deceased 2019).

Graduated @ IIT Kharagpur

- [14] Mahesh Shirole. Concurrency Test Scenario Generation using UML Transition Sequences. Feb. 2021.
- [15] Soma Saha. Unifying Heuristics and Evolutionary Computing for Characterizing and Solving Certain Combinatorial Optimization Problems, Sep. 2014.
- [16] Pravanjan Choudhury. Task Scheduling on Embedded Multiprocessors, Jan. 2013. (*with Prof. P.P. Chakrabarti*)
- [17] Subrat Kumar Panda. Simulation-Based Verification of Pipelined Processors, May 2010. (*with Prof. P.P. Chakrabarti*)
- [18] Vasant Patil. Efficient Algorithms for Video Transcoding, October 2009.
- [19] Dipankar Das. Functional and Performance Verification for Multiprocessor Embedded Applications, July 2009. (*with Prof. P.P. Chakrabarti*)
- [20] Sandip Aine. Design and Control of Anytime Algorithms, September 2008. (*with Prof. P.P. Chakrabarti*)
- [21] Pramod Singh. Multiobjective Combinatorial Optimization with Hybridization of EA, March 2008. (*with Prof. P.P. Chakrabarti*)
- [22] Durga Prasad Mohapatra. Dynamic Slicing of Object-Oriented Programs, September 2005. (*with Prof. R. Mall*)
- [23] Ashok Turuk. QoS Provisioning in WDM Networks, February 2005.

Master Thesis Supervision: M.Tech., MS (by Research), and MPhil

In progress @ JNU

- [1] Biraja Mishra. Anomaly Detection from Medical Time-Signals with Deep Learning. (by June 2023)
- [2] Luv Kumar. Hybridized Anomaly Detectors for Social Networks. (by June 2023)
- [3] Sai Teja Tangudu. Cost-Sensitive Learning for Class Imbalance Data. (by June 2023)

Graduated @ JNU

- [4] Khumaningthou Khumanthem. Interpreting outlier detection scores in ensembles. Jan. 2023.
- [5] Saurabh Tewari. Social network for blended learning model. Jan. 2023.
- [6] Junaciya K. Assessing generalization in medical image data. Oct. 2022.
- [7] Anish Sharma. Automata modelling with recurrent neural networks. Sep. 2022.
- [8] Trishita Mukherjee. Anomaly detection in complex graphs. Aug. 2022.
- [9] Pooja Singh. Assessing generalization in imbalanced datasets. Aug. 2022.
- [10] Gargi Mishra. Fairness in outlier detection ensembles. Aug. 2022.
- [11] Surabhi Shrivastav. Abnormality detection in medical images with deep learning. Oct. 2021.
- [12] Suresh Vyas. Meta-learning in ensembles. Oct. 2021.
- [13] Anjali Gautam. Time series prediction with deep learning. Sep. 2021.
- [14] Dipanjana De. Modelling automata by sequential neural networks. Aug. 2021.
- [15] Om Prakash. Anomaly detection in social networks. July 2021.
- [16] Akhilesh Rawat. Outlier detection in high dimensional datasets. June 2021.
- [17] Aditya Kumar. Assessing generalization in deep learning networks. April 2021.
- [18] Priti Kumar. Scientometrics of computer science publications in journals and conferences. July 2018.
- [19] Neha Kumari. A framework for runtime type genericity in Java generics. July 2018.
- [20] Akanksha Mukherji. Unsupervised outlier detection ensembles. July 2016.
- [21] Neeraj Pathak. Hybrid evolutionary bi-objective optimization. July 2016.

Graduated @ IIT Kharagpur

- [22] Mounika Kommuri, Test-case generation for concurrent sequence transitions, May 2012.
- [23] Surender Kumar, Static analysis of generic object oriented programs, May 2012.
- [24] S. Harikrishnan, Space efficient multi-method dispatch in object oriented languages, May 2012.
- [25] Gyan Baboo, Multiobjective combinatorial optimization with EA, May 2012.
- [26] Swadhin Barisal, Safer Java Programming Language Environment, May 2011.
- [27] Krunal Modi, Graph Topology Generation, May 2011. (with Nilanjan Banerjee, UArk)
- [28] Amit Suthar, Generation of test cases from UML diagrams using EAs, May 2010.
- [29] Abhiram Kasina, Trusted Computing, May 2010.
- [30] Mohammad Aslam, Multiobjective combinatorial optimization with GP, May 2010
- [31] Sri Harsha Dandibhotle, Multiobjective combinatorial optimization with EAs, May 2010
- [32] Siddharth Tiwary, Register Allocation May 2009. (with Jens Palsberg, UCLA)
- [33] Anchal Nema, Concurrent Programs, May 2009.
- [34] Neeraj Kumar, OO Testing, May 2009.
- [35] Kundan Singh, Synchronization and Race Condition, May 2009
- [36] Paresh Tolay, Multiobjective combinatorial optimization for Graph Coloring with GP, May 2009.
- [37] Bipul Kumar Bal, Multiobjective combinatorial optimization for BDMST with GP, May 2009
- [38] Krishna Banka, Multiobjective combinatorial optimization with GP, May 2008
- [39] Ashwin Joshi, Multiobjective combinatorial optimization with GP, May 2008
- [40] Soham Chakraborty, Type analysis & optimization of OO Systems, Feb. 2008. [with PPChak]
- [41] Sanjay Chaterjee, Algorithms for post-compilation power optimization in embedded processors, Dec. 2006. [with PPChak]
- [42] Arnab Sarkar, Low-Overhead Real-Time Proportional Fair Scheduling, May 2006. [with PPChak]
- [43] T. Kalyani, Video transcoding: H.264 to MPEG-II bit-streams, May 2006. [with JM]

- [44] Atul Bhartia, Video transcoding: Arbitrary resizing of H.264 bit-streams, May 2006. [with JM]
- [45] Anshuman Mishra, Software watermarking scheme for Java class files, May 2006. [with PPChak]
- [46] Vikram Agarwal, Multiple polymorphic arguments in object-oriented languages, May 2005.
- [47] M. Sreenivasulu, Code compression for performance enhancement of variable length embedded processors, May 2005.
- [48] Amit Gupta. Optimizing binaries using pattern matching, May 2005.
- [49] Mrinmoy Ghosh, Compiler backend generation and optimization for extensible architectures, June 2004. [with PPChak]
- [50] Sant Saran Gupta. Code compression in variable length RISC processors, May 2004.
- [51] Arnab Roy. Simulation based verification of microprocessor pipeline simulators, May 2004. [with PPChak]
- [52] Vasant Patil, Video transcoding algorithms, May 2004.
- [53] Anil Mangolia, Preprocessor for multimethods in object-oriented languages, Jan 2003.
- [54] GK Rao, QoS support for multimedia traffic with transcoding, Jan 2003.
- [55] K. Krishnakumar, RWA in WDM networks, May 2002.
- [56] Arun Kumar, Modelling of multimedia traffic, May 2002.
- [57] JS Rao, QoS support for multimedia traffic, Jan 2002.
- [58] B. Srikanth Reddy, Modelling of multimedia traffic, May 2001.

Graduated @ BITS Pilani

- [59] S. Prasanth, Network topology design using multiobjective EA, Dec 2000.
- [60] MS Sudarshan, Network topology design using multiobjective EA, Dec 2000
- [61] Ishan Banerjee, Polymorphism in cipher design, Dec 2000
- [62] V. Devtha, Video temporal segmentation, Dec 2000.
- [63] KL Arvind, Visual modelling, May 2000. [with IJN]
- [64] Mayank Gupta, Hybrid learning algorithm for vector quantization, May 2000.
- [65] Bhanu Prakash, Hybrid learning algorithm for vector quantization, May 2000.
- [66] Vinay Seth, Finger print identification, May 2000.
- [67] Vishnu Makkapati, Encoding of multi-/hyper-spectral image data, Dec 1999.
- [68] Mahesh Agarwal, Face recognition, Dec 1999.
- [69] SK Santhanakrishnan, Communication network topology design using multiobjective EA, Dec 1999.
- [70] N. Vinay Kumar, Object oriented toolkit for multiobjective EA, May 1999.
- [71] Dilip Jain, Multiobjective optimization with evolutionary algorithm, May 1998. [with NC]
- [72] ... and several others.

Supervision of Research Associates in Consultative/Sponsored Projects: IIT Kharagpur

- [1] Pritam Khanda, MHRD's Virtual Lab, 2010-11.
- [2] Soham Chakraborty, Microsoft's Object-Oriented Technology, 2004-06.
- [3] P. Sankar Muthu, MHRD's Multiobjective EAs for Combinatorial Optimization Problems, 2004-05.
- [4] Panchali Sen, Microsoft's Object-Oriented Technology, 2003-04.
- [5] Dipankar Das, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [6] Sandip Aine, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [7] Arnab Sarkar, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [8] Vibha Rathi, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [9] Sanjay Chaterjee, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [10] Samik Das, NSC's Software Tools for Embedded Systems, 2003-04. [jointly with PPChak]
- [11] Subrat Panda, NSC's Software Tools for Extensible CompactRISC Processors, 2002-04. [jointly with PPChak]
- [12] Rakesh Gupta, NSC's Software Tools for Extensible CompactRISC Processors, 2002-03. [jointly with PPChak]
- [13] Mrinmoy Ghosh, NSC's Software Tools for Extensible CompactRISC Processors, 2001-04. [jointly with PPChak]
- [14] Banit Agarwal, NSC's Software Tools for Extensible CompactRISC Processors, 2001-03. [jointly with PPChak]
- [15] Debjit Sinha, NSC's Software Tools for Extensible CompactRISC Processors, 2001-02. [jointly with PPChak]
- [16] Supratik Mazumdar, NSC's Software Tools for Extensible CompactRISC Processors, 2001-02. [jointly with PPChak]
- [17] Rahul Chaudhry, NSC's Software Tools for Extensible CompactRISC Processors, 2001-02. [jointly with PPChak] [@ Yale U.]

* * *

Selected Sponsored/Consultancy Projects:: Completed

[1] *Sponsored Project* : Educational Data Mining
Funded by : UGC, Govt. of India
Period : 2018 – 19
Principal Investigator : Rajeev Kumar *Amount* : INR 500 K
Co-Investigator : ---

[2] *Sponsored Project* : Programming and Data Structures Virtual Lab
Funded by : MHRD, Govt. of India
Period : 2010 – 12
Principal Investigator : Rajeev Kumar *Amount* : INR 1500 K
Co-Investigator : ---

The course is the basic core course which is usually done by every engineering student of all engineering branches in their first year of study. A student is introduced to the basic concepts of programming and an algorithmic approach to problem solving. Since this is the first course in programming, the lab plays a very important role in understanding the programming concepts and problem solving using computer. In the absence of a lab, this course remains an abstract course. Through the lab, a student understands the intricacies of programming and problem solving.

For programming, the general and most versatile, C-language is being used currently.

However, the next phase of Virtual Lab can have Java programming language as the medium of the problem solving.

[3] *Consultancy Project* : Object Oriented (C#/NET centric) Courseware Development
Funded by : Microsoft Corp., USA (*under Global CFP*)
Period : 2004 – 07 *Amount* : USD 22 K
Principal Consultant : Rajeev Kumar *Co-Consultant* : ---

The course is designed primarily to bring synergy and interplay among the following components – Object Oriented Technology (OOT) for better programming methodologies and code reuse, Programming Language Design and Implementation (PLDI) for better understanding of language semantics, Virtual Execution Environment (VEE) for an insight of the runtime system providing extensible and safer computing towards code certification, and Software Engineering (SE) for trusted and reusable system.

The course was introduced as a graduate level course at IIT Kanpur and is offered since then at IIT Kharagpur. Many research articles are the outcome of this activity.

[4] *Sponsored Project* : Multiobjective Evolutionary Algorithms for Combinatorial Optim.
Funded by : MHRD, Govt. of India
Period : 2002 – 07 *Amount* : INR 1000 K
Principal Investigator : Rajeev Kumar *Co-Investigator* : ---

Most combinatorial problems are computationally *hard*. In a multiobjective setting, the major challenge is to obtain a set of many representative diverse solutions across the desired/optimal Pareto-front. In simple treatments, multiple objectives are combined in an *ad hoc* manner to yield a scalar objective. Other conventionally used optimization heuristics model the multiobjective problem in a single objective domain treating all but one of the objectives as constraints. In this project, the standard single objective problems (with or without constraints), e.g., spanning tree, 0/1 knapsack, traveling salesman, graph coloring etc., are recasted into their respective biobjective optimization problems. Empirically, it is shown that the solutions obtained with multiobjective evolutionary algorithms (MOEA) as black-box optimization tools, cover a larger range and are superior, in terms of diversity and convergence, to the deterministic heuristics.

Outcome of this sponsored research project is reflected in many research publications. Another, important spin-off of this research activity is that the tutorial on “Evolutionary Multiobjective Combinatorial Optimization (EMCO),” which became a regular feature at ACM’s GECCO till 2009, the lead conference in this area.

[5]	<i>Consultancy Project</i>	: Software Tools for Embedded Systems	
	<i>Funded by</i>	: National Semiconductors Corp., Germany/USA	
	<i>Period</i>	: 2003 – 06	<i>Amount:</i> USD 100 K
	<i>Principal Consultant</i>	: Prof. P.P. Chakrabarti	<i>Co-Consultant</i> : Rajeev Kumar
[6]	<i>Consultancy Project</i>	: Software Tools for CR Family of Processors	
	<i>Funded by</i>	: National Semiconductors Corp., Germany/USA	
	<i>Period</i>	: 2001 – 03	<i>Amount:</i> USD 225 K
	<i>Principal Consultant</i>	: Prof. P.P. Chakrabarti	<i>Co-Consultant</i> : Rajeev Kumar

These two consultancy projects were due to the collaborative research between IIT Kharagpur and the Compact RISC (CR) Processing groups of National Semiconductor Corporation (NSC) located at Germany and Israel. University of Michigan (UoM) was another partner in this activity looking into the hardware related aspects whereas IIT Kharagpur concentrated on software related issues.

Twin projects were focused on development of software tools for rapid reduction in the time to market a custom solution based on the CR family of processors. This included software development tools, architecture exploration and evaluation tools and Instruction Set Architecture (ISA) verification tools. The aim was to help NSC develop tools that would speed up the development of new architectures (like functional and performance simulators), produce systems software tools (like Debuggers, Assemblers, Linkers, Compilers) for customers and rapidly explore the design space by providing tools for extensible CR processors (ISA verification and software toolset for CR-X). This effort also aimed at developing the Software Quality Assurance (QA) framework for the tools produced by this activity.

The project activity had three primary parts, namely tools for CR16C, extension of the toolset to handle CR16C family of processors, and development of scalable toolset concept for the extensible CR (CR-X). The CR-X activity also aimed at developing a mechanism so that the toolset can be automatically generated for CR based processors. Generation of tests and testing the software in a proper QA framework was a very important goal. The platform for development was Linux. The effort aimed at maximum use of available GNU following the GNU rules/coding standards.

While working on these twin projects, the project team encountered many interesting issues and challenges, solutions to some of those culminated in several research publications.

[7]	<i>Sponsored Project</i>	: Convergence of Multiobjective Evolutionary Algorithms	
	<i>Funded by</i>	: IIT Kharagpur	
	<i>Period</i>	: 2001 – 03	<i>Amount:</i> INR 100 K
	<i>Principal Investigator</i>	: Rajeev Kumar	<i>Co-Investigator</i> : ---

In multiobjective optimizations, solutions which are non-dominated at some stage in the computations, become dominated by a superior solution at some later stage. Moreover, for most of the real-world problems, the desired/optimal Pareto front is *unknown*. This project was aimed at extending the previous work (GALESIA, 1997) and to incorporate the notion of convergence in multiobjective evolutionary algorithms (MOEA).

* * *